

8

7

6

5

4

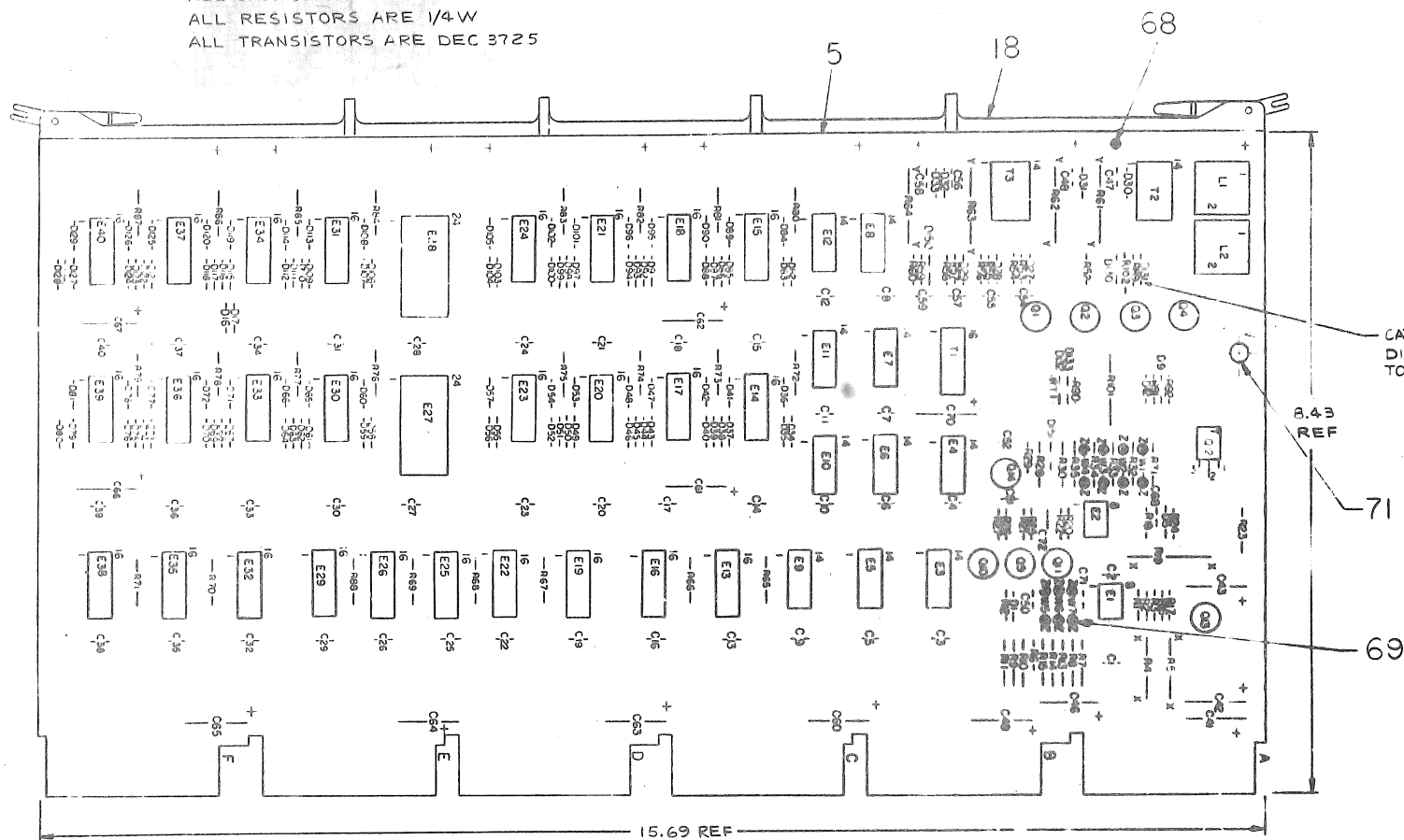
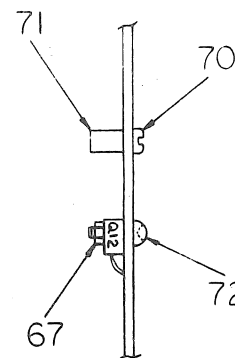
3

2

1

NOTES:

1. * INDICATES NOT USED ON MF11-U & MF11-UP (2 PLACES).
2. ** INDICATES NOT USED ON MF11-U & MF11-UP, BUT ARE TIED TO UNUSED TERMINATORS ON THE 6114 MODULE, WHICH FORCES THEM TO +2V (5 PLACES).
3. 1 THERMISTOR LOCATED ON H217 STACK MODULE, 1 ON 6235
4. UNLESS OTHERWISE INDICATED;
ALL DIODES ARE D672
ALL CAPACITORS ARE .01 Uf
ALL RESISTORS ARE 1/4-W
ALL TRANSISTORS ARE DEC 3725

CATHODE END OF ALL
DIODES SHALL BE
TOWARD FINGERS

FV2, AA1

AA2, BA2, CA2,
DA2, EA2, FA2AC2, AT1, BC2, BT1,
CC2, CT1, DC2, DT1,
EC2, ET1, FC2, FT1,
AM1

AK1

+20V

+5V

-5V

74121	7	—	—
741	—	—	—
7532E	—	9	16
7442	8	16	—
74154	12	24	—
IC TYPE	GND	+5V	+20V

GND AND 5V ARE USUALLY PIN 7 AND 14
RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE

IC PIN LOCATIONS

8

7

4

3

2

1

FIRST USED ON OPTION MODEL
MF11-U & MF11-UP

QTY	REF DESIGNATION	DESCRIPTION	PART NO	ITEM NO
PARTS LIST				
ETCH BOARD REV D				
DRN CHK'D ENG PROJ ENG PROD DATE 2-2-73 DATE 4-4-73 DATE 11/1/73 DATE 11/1/73 DATE 11/1/73				
NEXT HIGHER ASSY				
DEC NO EIA NO DEC NO EIA NO				
SEMICONDUCTOR CONVERSION CHART				
SCALE 1/2 SHEET 2 OF 6				
DCS 6235-0-1 REV L				

SECTION DCS
NUMBER 6235-0-1

B

A

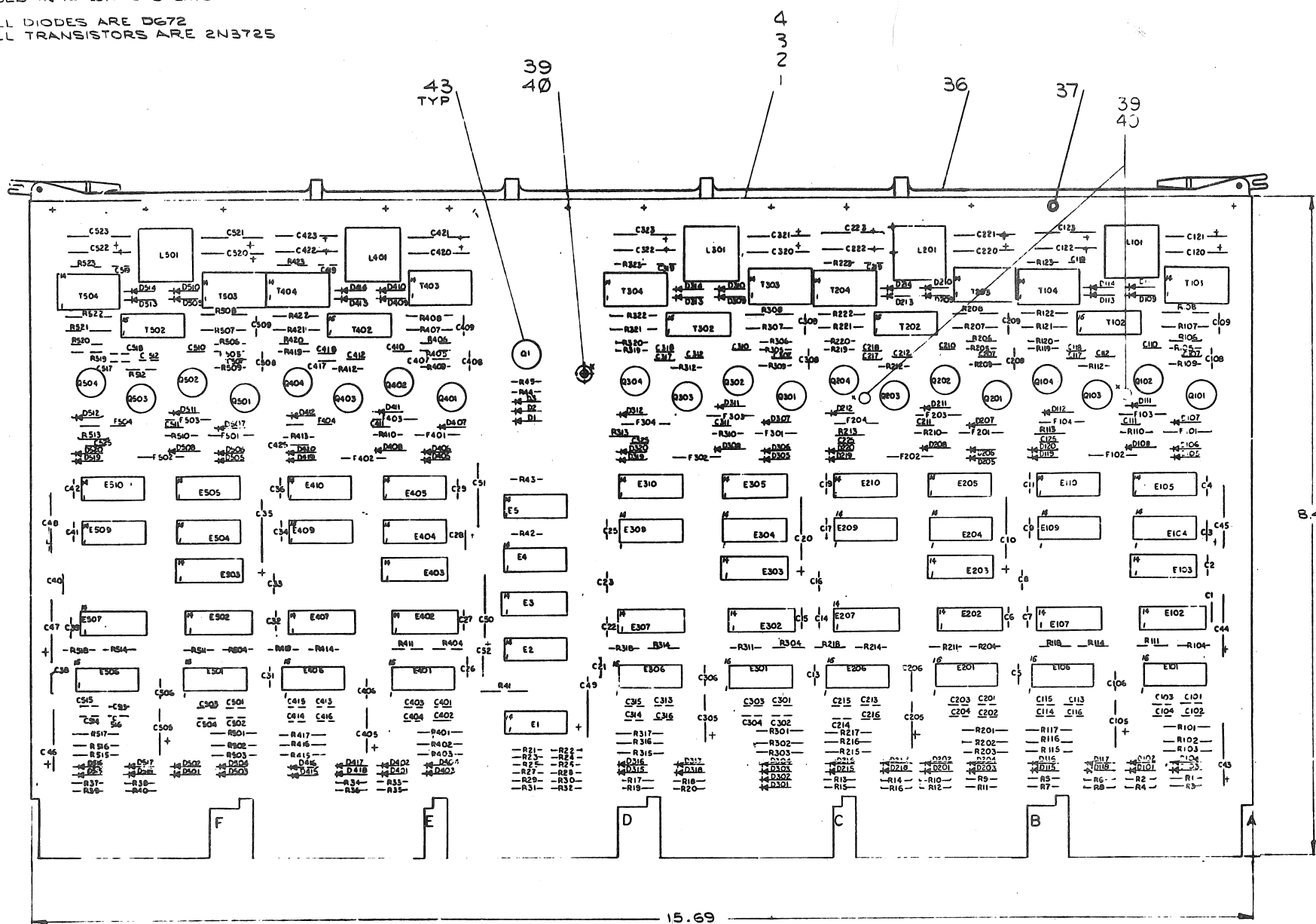
8		7		6		5		4		3		2		1	

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NOTES: UNLESS OTHERWISE SPECIFIED

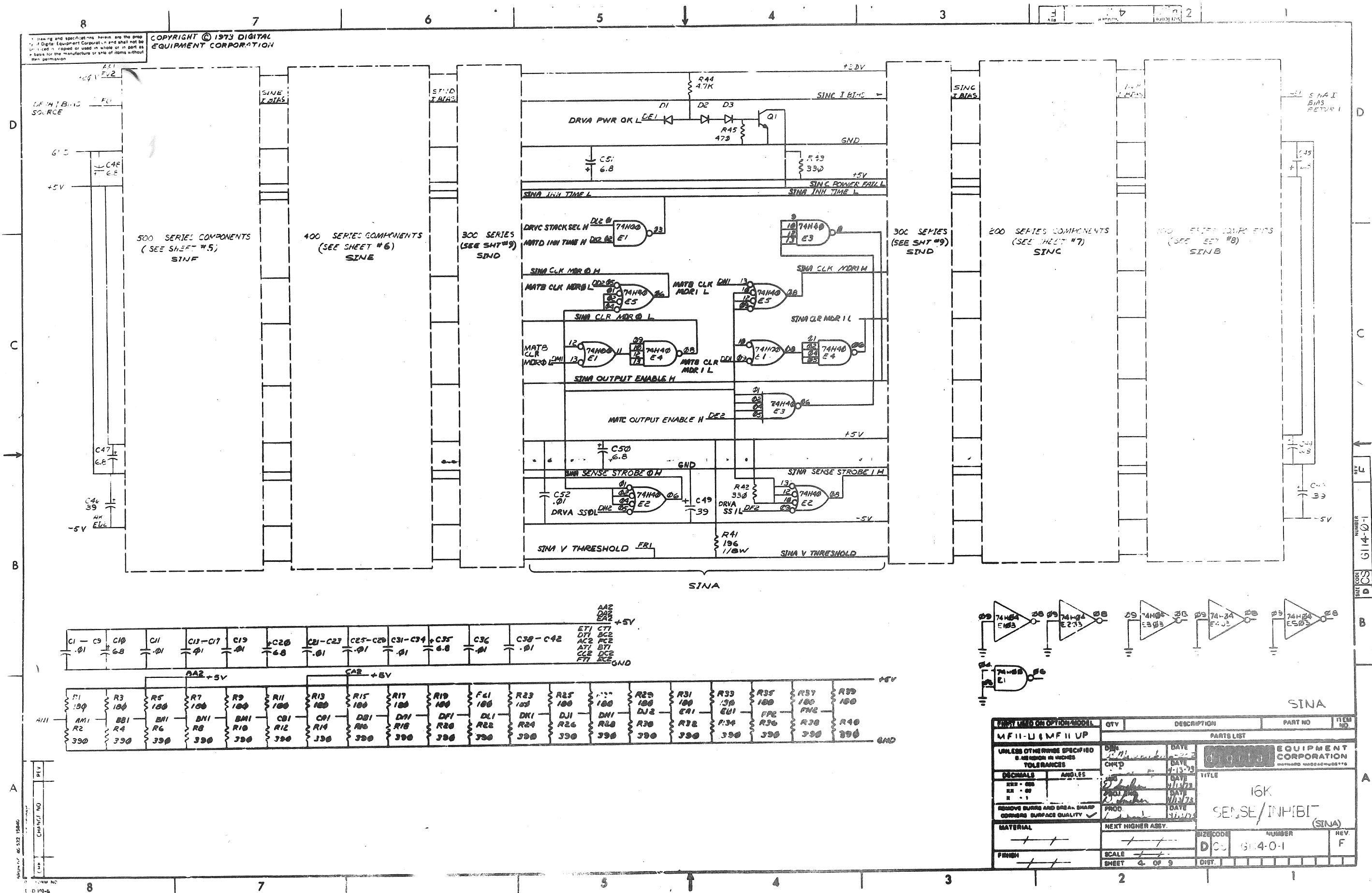
1. ALL RESISTORS ARE IN OHMS 1/4W
2. ALL CAPACITANCE IS IN MICROFARADS
3. DATA BITS 7 & 18 ARE NOT USED IN 8 BIT SYSTEMS
4. DATA BIT 18 IS NOT USED IN 16 BIT SYSTEMS
5. DATA BITS 16, 17, 18 & 19 ARE NOT USED IN 16 BIT SYSTEMS
6. ALL DIODES ARE DG72
7. ALL TRANSISTORS ARE 2N3725

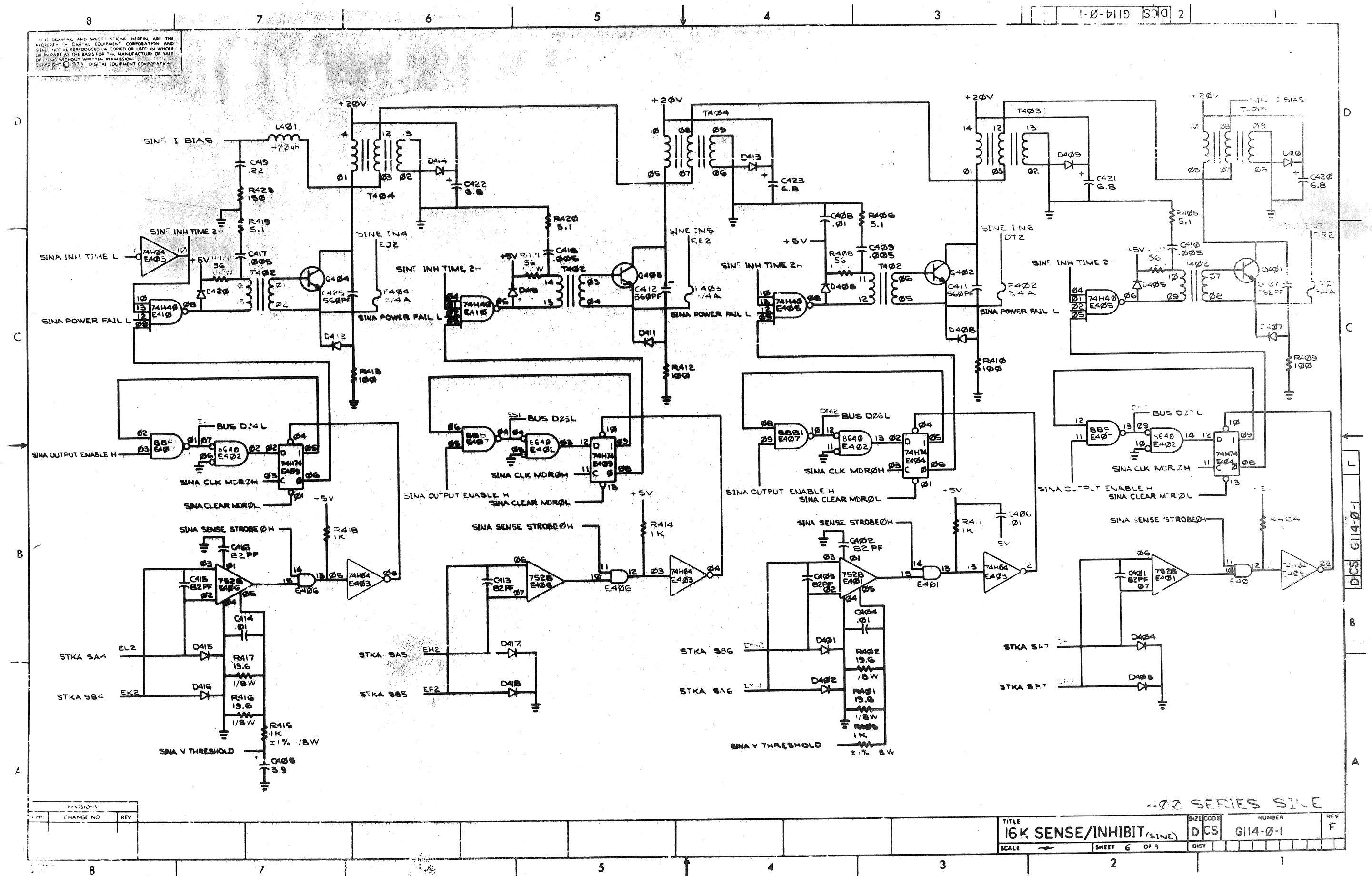


IC DEC 130	1	8	-
IC DEC 8640	1	8	-
IC DEC 7528	9	16	8
IC TYPE	GND	+5V	-5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.			
IC PIN LOCATIONS			

QTY	REF DESIGNATION	DESCRIPTION	PART NO	ITEM NO
FIRST USED ON OPTION MODEL				
MF 11-U & MF 11-UP				
ETCH BOARD REV C				
PARTS LIST				
DATE	4-3-73	DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
DATE	7-1-73	TITLE 16 K SENSE/INHIBIT		
DATE	7-1-73	NEXT HIGHER ASSY		
DATE	7-1-73	SCALE		
DATE	7-1-73	SHEET 2 OF 9		
DEC NO.	EIA NO.	DEC NO.	EIA NO.	REV. F
SEMICONDUCTOR CONVERSION CHART				
SIZE CODE DCS NUMBER G114-0-1				
DIST.				

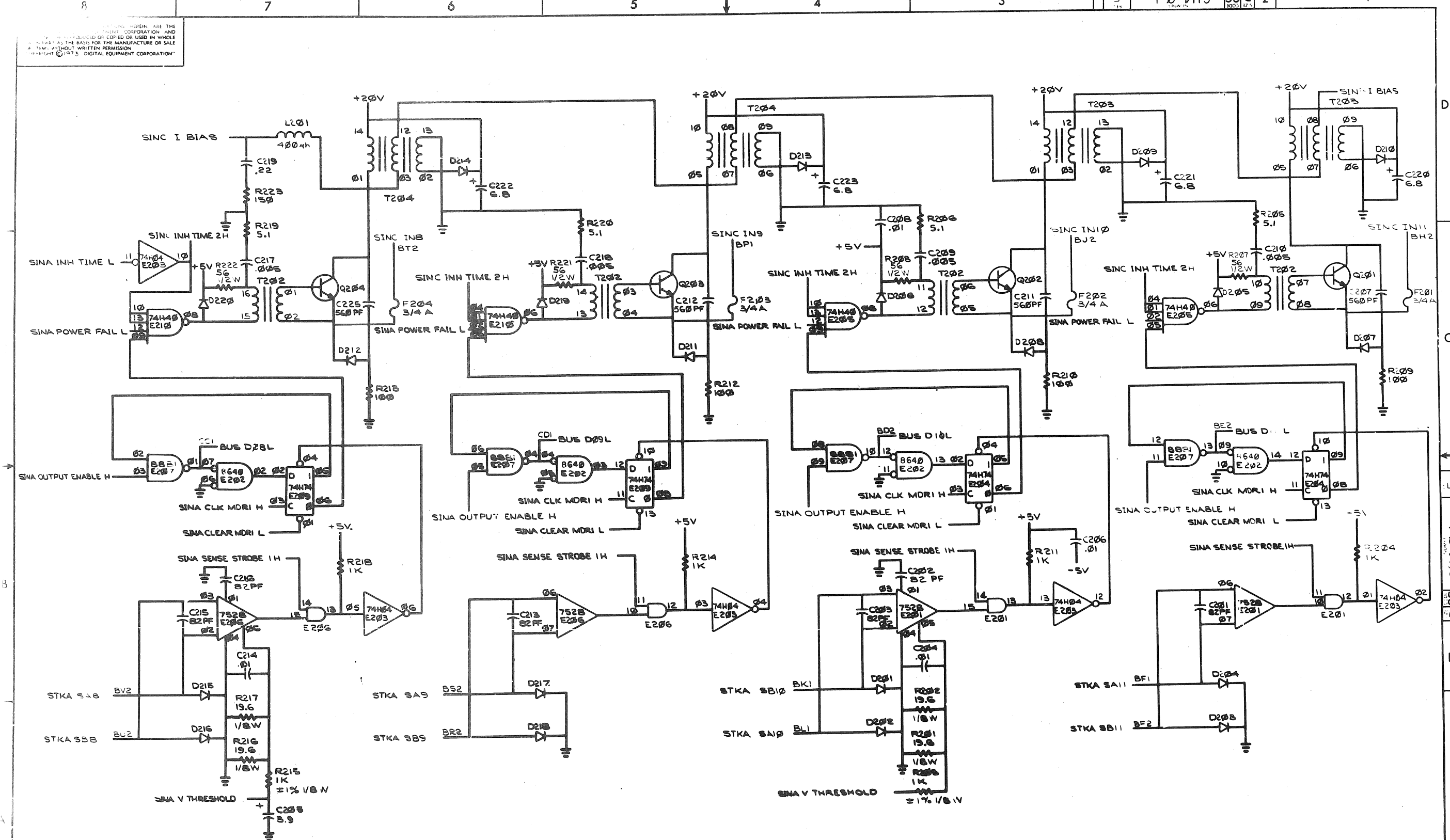
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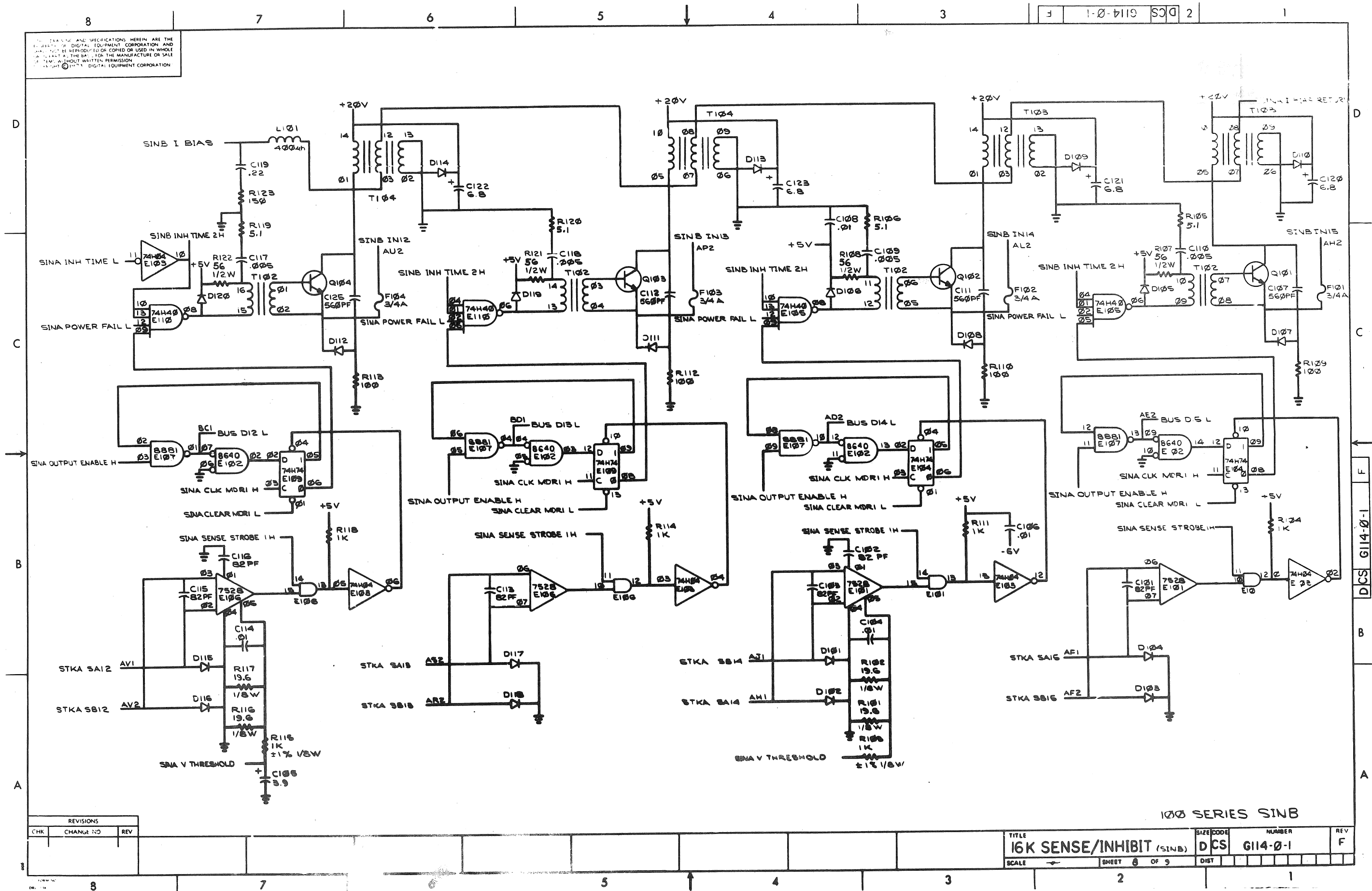
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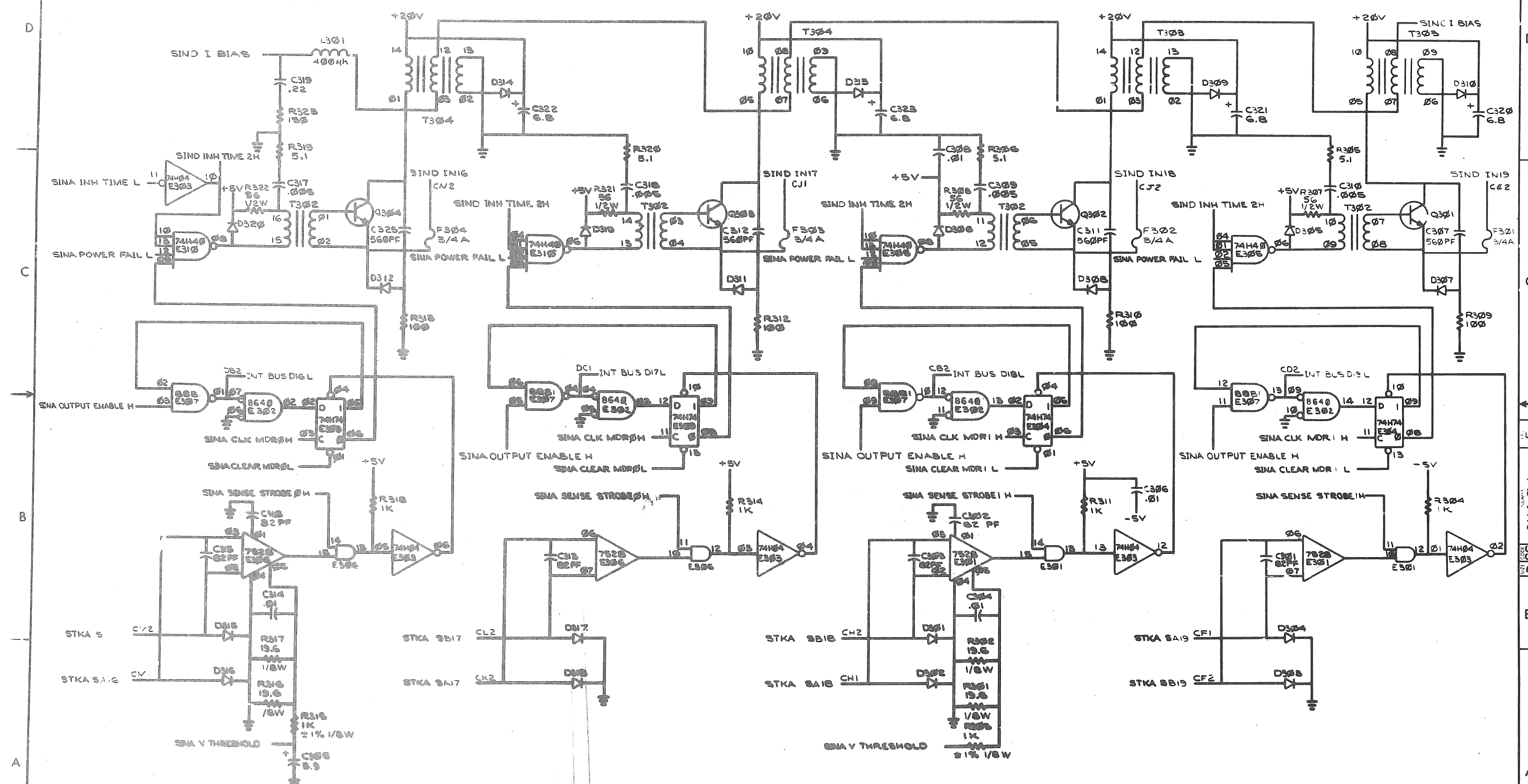
1-0-0119 DCS 2



REVISIONS			TITLE		SIZE CODE		NUMBER		REV.	
CHK	CHANGE NO	REV	16K SENSE/INHIBIT (SINC)		DCS		G114-0-1		F	
			SCALE		SHEET 7 OF 9		DIB.			

200 SERIES SINC



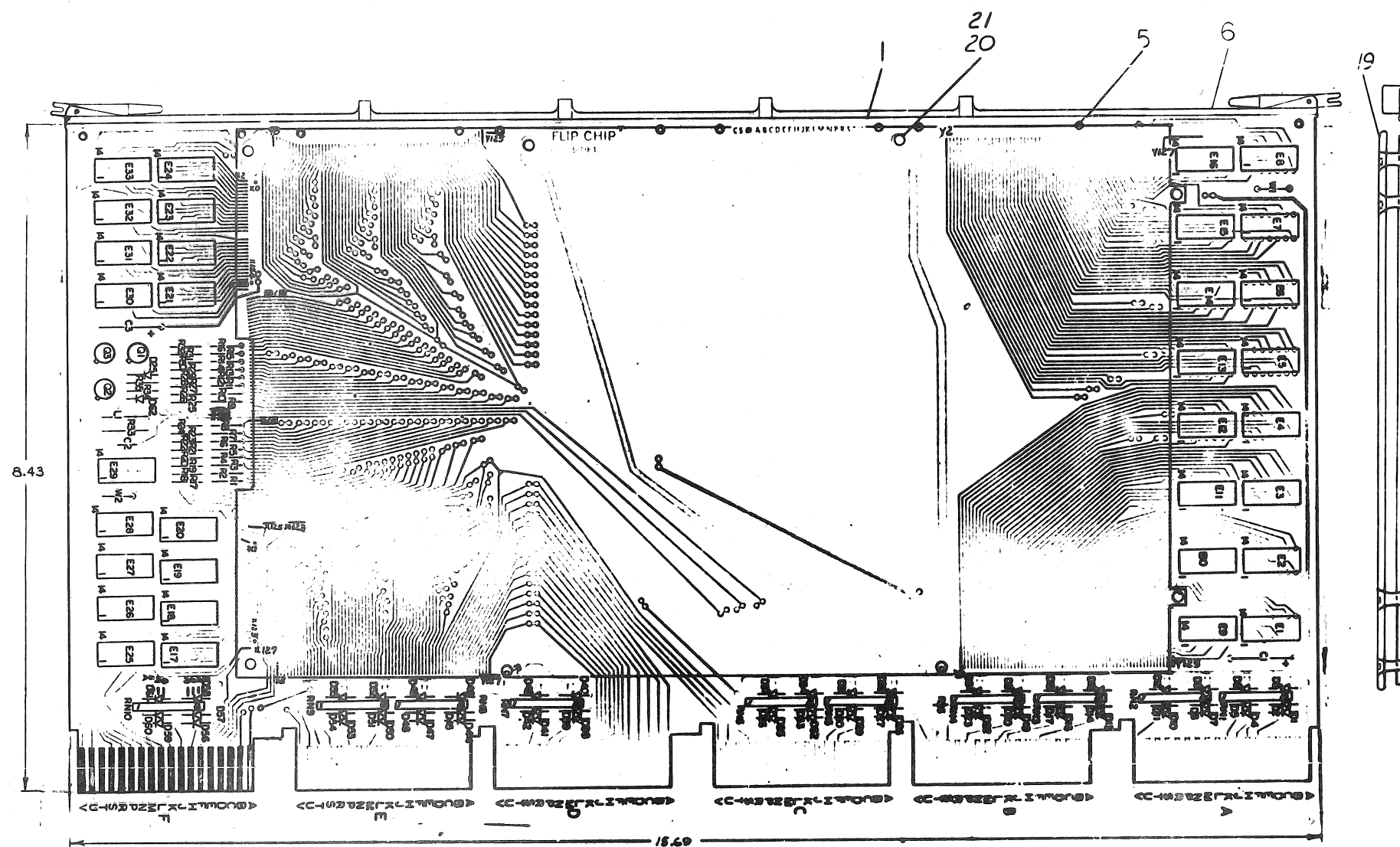


REVISIONS		
CHK	CHANGE NO	REV

1. — X0 THRU X127 — & — Y0 THRU Y127 —
INDICATES MAGNET WIRE TERMINATION
(SOLDERED TO PRINTED CIRCUIT PAD).

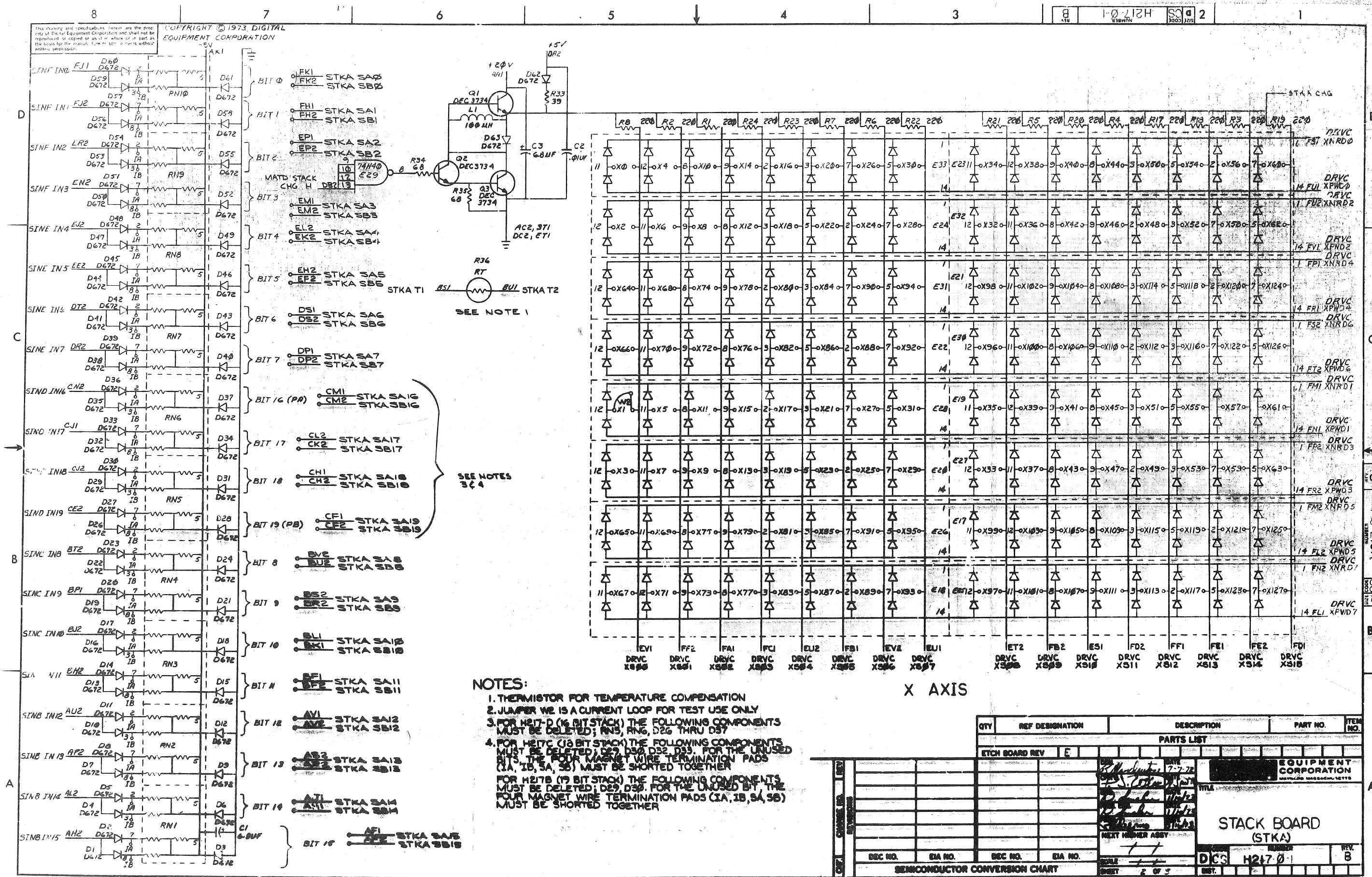
2. FOR ASSEMBLY INFORMATION & PART NO'S.
OF ITEMS NOT CALLED OUT ON THIS DRAWING
REFER TO E. AD-9305680-0-0 (MEMORY
STACK ASSY).

LEGEND			
NUMBER	DEF	PURCHASE SPEC	DEC PART NO. STACK CONFIGURATION
9305680-1			H217-A 16K X 0
9305680-2			H217-B 8K X 19
9305680-3			H217-C 16K X 18
9305680-4			H217-D 16K X 16



QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
1	R36	RES 300 2% THERMAL DISC	1303785	19
1	L1	CHOKE 100UH 10%	16 10660	17
3	Q1, Q2, Q3	TRANSISTOR DEC 334	15 10062	16
32	R1 THRU R32	RES. 220, 1/4W 5%	13 00271	15
2	R34, R35	RES. 20, 1/4W 5%	13 00219	4
1	R33	RES. 39, 1/2W 5%	13 02336	3
1	RNI THRU RNI10	RES NETWORK 2-5K, 4-.1K	13 10964	12
1	E29	IC DEC 74H40	19 05386	11
32	E1 THRU E28, E30 THRU E33	IC DEC. 2501 (16 DIODES)	19 0000	10
68	D1 THRU D64	DIODE D672	11 05115	9
2	C1, C3	CAP. 2.5UF. 35V 10% 5 TANT	13 05300	8
1	C2	CAP. 0.1UF 50V 20%	13 01610-00	7
				6
				5
				4
				3
		X-Y COORDINATE HOLE LOCATION	KC-04217-B-4	2
		ETCH CRT BOARD (G 647)	50100-3	1

[illegible][illegible]



NOTES:

1. THERMISTOR FOR TEMPERATURE COMPENSATION
2. JUMPER WE IS A CURRENT LOOP FOR TEST USE ONLY
3. FOR H217-D (16 BIT STACK) THE FOLLOWING COMPONENTS MUST BE DELETED; RNS, RNS, D26 THRU D37
4. FOR H217C (13 BIT STACK) THE FOLLOWING COMPONENTS MUST BE DELETED; D29, D30, D32, D33. FOR THE UNUSED BITS, THE FOUR MAGNET WIRE TERMINATION PADS (IA, IB, SA, SB) MUST BE SHORTED TOGETHER

FOR H217B (19 BIT STACK) THE FOLLOWING COMPONENTS MUST BE DELETED; D29, D30. FOR THE UNUSED BIT, THE FOUR MAGNET WIRE TERMINATION PADS (IA, IB, SA, SB) MUST BE SHORTED TOGETHER

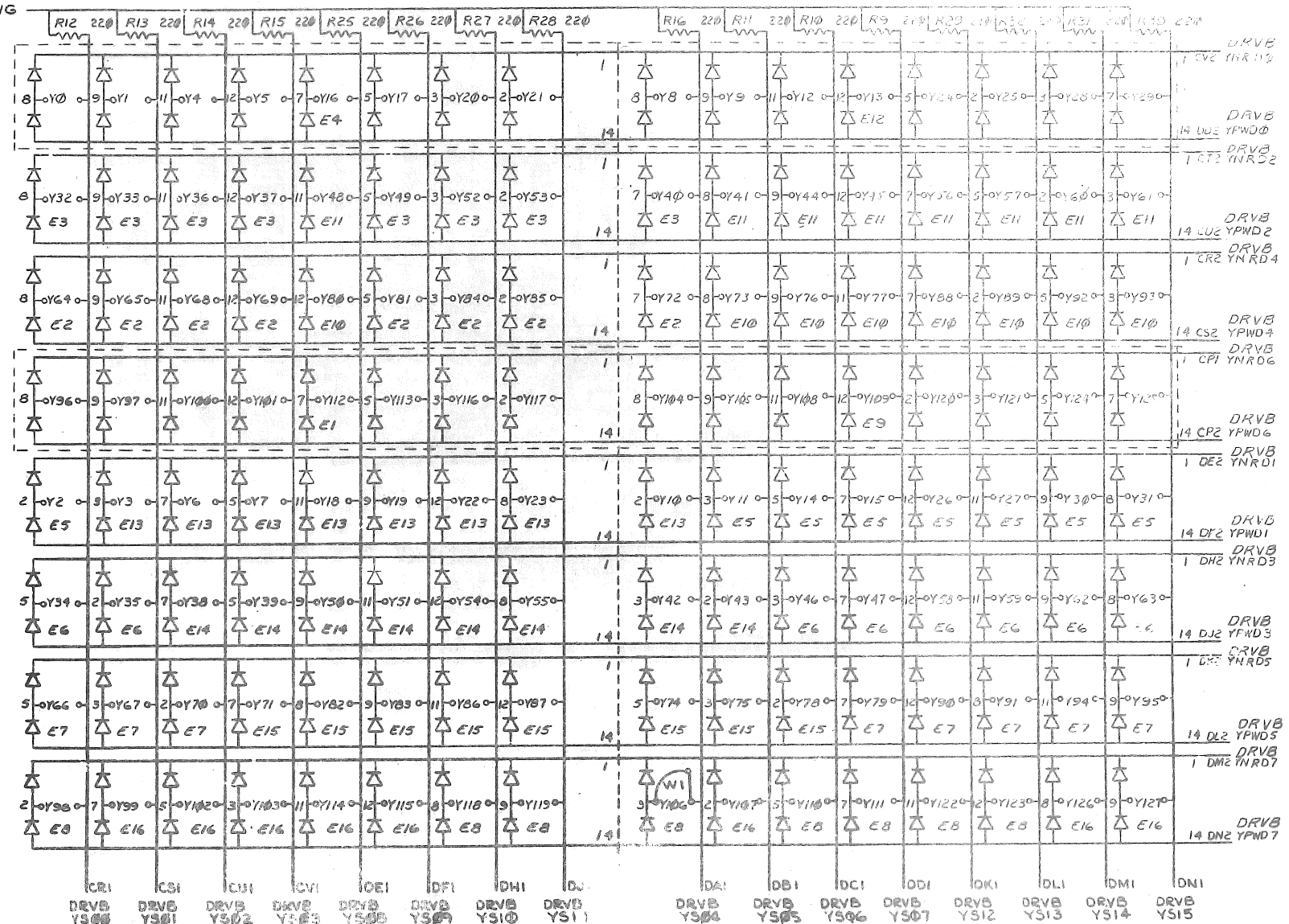
X AXIS

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV E				
EQUIPMENT CORPORATION				
STACK BOARD (STKA)				
H217-0-1				
REV B				
SEMICONDUCTOR CONVERSION CHART				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	
1	100	2	200	
3	300	4	400	
5	500	6	600	
7	700	8	800	
9	900	10	1000	
11	1100	12	1200	
13	1300	14	1400	
15	1500	16	1600	
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99	9900	100	10000	

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STKA CHG



QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV E				
EQUIPMENT CORPORATION NORWOOD, MASSACHUSETTS				
STACK BOARD (STKB)				
DCS H217-0-1				
REV B				