NOTE: TO INSTALL MF11-U OR MF11-UP IN 11/40 CPU WITH SERIAL NUMBER LESS THAN 6000 OR H968-D (OR-E) WITH SERIAL LESS THAN 7000 A FIELD MODIFICATION KIT (FM11-UA, -UB, OR -UC) IS REQUIRED.
<table>
<thead>
<tr>
<th>REF NO.</th>
<th>DRAWING NO.</th>
<th>REV</th>
<th>NO OF SH</th>
<th>DESCRIPTION</th>
<th>OPTION NO / FILE DATE</th>
<th>MECHANICAL</th>
</tr>
</thead>
<tbody>
<tr>
<td>X</td>
<td>A-PL-MF11-U-0</td>
<td>A</td>
<td>1</td>
<td>16K SENSE MEMORY</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-CS-G114-0-1</td>
<td>#</td>
<td>9</td>
<td>16K SENSE INHIBIT</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-CS-G235-0-1</td>
<td>#</td>
<td>9</td>
<td>16K X-Y DRIVE</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-CS-MB504-0-1</td>
<td>#</td>
<td>10</td>
<td>16K MEMORY TIMING</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-CS-MZ729-0-1</td>
<td>#</td>
<td>3</td>
<td>MEMORY STACK</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-MI-MF11-U-MU</td>
<td>A</td>
<td>1</td>
<td>MODULE UTILIZATION</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-TI-MF11-U-1</td>
<td>A</td>
<td>1</td>
<td>TIMING DIAGRAM</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-CS-MZ729-0-1</td>
<td>#</td>
<td>3</td>
<td>PARITY CONTROL</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>A-SF-MF11-U-3</td>
<td>*</td>
<td>2</td>
<td>CUSTOMER ACCEPTANCE PROCEDURE</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>A-SF-MF11-U-2</td>
<td>*</td>
<td>6</td>
<td>ONLINE TEST PROCEDURE</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>A-SF-MF11-U-4</td>
<td>*</td>
<td>3</td>
<td>MF11-U ENG SPEC.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-AD-7009295-0-0</td>
<td>#</td>
<td>1</td>
<td>BACK PLANE ASSY</td>
<td></td>
<td></td>
</tr>
<tr>
<td>X</td>
<td>D-CS-5410345-0-1</td>
<td>#</td>
<td>1</td>
<td>32K X 16.18 MEMORY BACKPLANE</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**CUSTOMER PRINT SET**
- X = PRINT OF DOCUMENT INCLUDED IN PRINT SET
- C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
- S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

**MECHANICAL**
- TITLE: 16K SENSE MEMORY
- SHEET 3 OF 3
- SIZE CODE: B
- DD CODE: MF11-U
- REV: A
<table>
<thead>
<tr>
<th>IC TYPE</th>
<th>IC NO.</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**NOTES:**
1. FOR 162... (USE WITH PART 51), USE IC 156-398Y-2.
2. IC 156-398Y-2 USES WITH IC 156-471 ONLY.
3. INSTALL 1 MODULES FOR PART 158, 2 MODULES FOR IC 158.

**VIEWED FROM MODULE SIDE OF BACKPLANE**
NOTES:

1. ***IN THE DATACLK MODE CLEAR NRML ONLY OCCURS IN THE BYTE UNIT BEING ADDRESSED.
2. ***IN THE DATACLK MODE CLOCK NRML ONLY OCCURS IN THE BYTE UNIT BEING ADDRESSED.
3. ***ACTUAL TIME DEPENDS ON BUS AND PROCESSOR DELAYS.
4. BUS CLEAR NRML IS 20 MS LATER THAN SHOWN FOR DATACLK-DATACLK MODE.

BUS CLR AND 150 MS LATER FOR DATACLK-DATACLK MODES.
<table>
<thead>
<tr>
<th>RT</th>
<th>REF DESIGNATION</th>
<th>DESCRIPTION</th>
<th>PART NO.</th>
<th>STATUS</th>
<th>REV</th>
<th>REASON</th>
</tr>
</thead>
</table>
| 1 | D2 | VMware | 105463 | 09 | 00 | -
| 2 | E10 | CB08514444 | 105464 | 09 | 00 | -
| 3 | 47.21 | CB02614444 | 105465 | 09 | 00 | -
| 4 | 01251-A911-CB087921-EB04-T3322 | CB097025 | 105466 | 09 | 00 | -
| 5 | 025 | CB087921 | 105467 | 09 | 00 | -
| 6 | 69.12 | CB087921 | 105468 | 09 | 00 | -
| 7 | 116 | CB087921 | 105469 | 09 | 00 | -
| 8 | 106 | CB087921 | 105470 | 09 | 00 | -
| 9 | 54 | CB087921 | 105471 | 09 | 00 | -
| 10 | 102 | CB087921 | 105472 | 09 | 00 | -
| 11 | 50 | CB087921 | 105473 | 09 | 00 | -
| 12 | 51-30 | 105474 | 09 | 00 | -
| 13 | 52 | 105475 | 09 | 00 | -
| 14 | 53 | CB087921 | 105476 | 09 | 00 | -
| 15 | 54 | CB087921 | 105477 | 09 | 00 | -
| 16 | 55 | CB087921 | 105478 | 09 | 00 | -
| 17 | 56 | CB087921 | 105479 | 09 | 00 | -
| 18 | 57 | CB087921 | 105480 | 09 | 00 | -
| 19 | 58 | CB087921 | 105481 | 09 | 00 | -
| 20 | 59 | CB087921 | 105482 | 09 | 00 | -
| 21 | 60 | CB087921 | 105483 | 09 | 00 | -

**REVISION**

<table>
<thead>
<tr>
<th>ITEM</th>
<th>CHANGE NO.</th>
<th>REMARK</th>
</tr>
</thead>
</table>
| 1 | | -
| 2 | | -
| 3 | | -
| 4 | | -
| 5 | | -
| 6 | | -
| 7 | | -
| 8 | | -
| 9 | | -
| 10 | | -

**DRAWING SHEET**

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
</tr>
</thead>
<tbody>
<tr>
<td>8</td>
<td>7</td>
<td>6</td>
<td>5</td>
</tr>
</tbody>
</table>

**SEE REVISION HISTORY**

- **REQUESTED CHANGE**
- **APPROVED CHANGE**
<table>
<thead>
<tr>
<th>SH NO.</th>
<th>PAGE REVISIONS</th>
<th>REMARKS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>A B C</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>A B C</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>A B C</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>A B C</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>A B C</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>A B C</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>A B C</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>A B C</td>
<td></td>
</tr>
</tbody>
</table>

**DATE:** 4/4/73
**ENG. REV.** A
**CHECKED:** 4/4/73
**DRAWN:** W. Major
**TITLE:** 16K SENSE/INHIBIT

**NEXT HIGHER ASSY.:** B-DD-MMII-U
**SIZE CODE:** B CS G114 - Ø - 1
**REV.:** C

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NOTE: JUMPER W1 IS A CURRENT LOOP FOR TEST USE ONLY.
**ENGINEERING SPECIFICATION**

**TITLE** MF11-U/UP CUSTOMER ACCEPTANCE PROCEDURE

---

**1.0 Overview**

1.1 This procedure contains directions pertaining to field assurance of the correct operation of an MF11-U/UP.

1.2 Prior to this acceptance, the option will have been installed, inspected, and connected and have power applied. Generally the memories shipped as add ons will have been configured and tested at the factory with their addresses beginning at 000000. Prior to running diagnostics, these memories must be re-configured for the customers system, as directed in the MF11-U/UP Customer Print Set. (See M2934 MAT A)

1.3 If this option is part of a PDP-11/40-11/45 installation, as opposed to an add on, then the system acceptance procedures provided with those systems should be utilized in place of this procedure.

---

**2.0 Inspection**

2.1 Assure presence of the following documentation:

2.1.1 Customer Acceptance Form

2.1.2 Keysheets (2)

2.1.3 Accessory Checklist

2.1.4 LIBKIT list for MF11-U/UP

2.1.5 ECO Status Sticker (for mounting in expansion box)

2.1.6 Waiver Sheet (if applicable)

2.1.7 Documentation Update Card (if applicable)

2.2 Utilize the accessory checklist, and LIBKIT list to verify that all items are present.

---

**3.0 Diagnostic Testing:**

3.1 The following tests must be run without error for the times specified:

- Maindec-11-DQQA Memory I/O 5 min/MF11-U/UP
- Maindec-11-DQMQ Memory Exerciser 5 min/MF11-U/UP
- Maindec-11-DCMFA Parity Test 2 passes/MF11-U/UP

*(MF11-U/UP Memory Only)*

*Only for add-on memories in systems with NDR devices*