

digital

212

MM11-DP

Engineering Drawings

Digital Equipment Corporation

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B-TC-MM11-DP-3

[illegible]

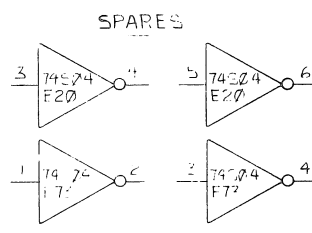
DEC 16-13251-1063-1A-8032

REVISIONS			
DATE	CHG. NO.	REV	
MAY 76	MM11DP-1	A	

USED ON OPTION/MODEL		DRN.	DATE	TITLE									
		W. LUFKIN	24 OCT 75	MM11-DP MEMORY MODULE									
		CHK'D	DATE										
		W. Lufkin	24 OCT 75										
		PROJ. ENG.	DATE										
		D. Smith	10/28/75										
		PROD.	DATE	SIZE	CODE	NUMBER					REV		
		R.A. Hill	10/29/75	B	DD	MM11-DP					A		
		FIELD SERV.	DATE	DIST									
SHEET	1 OF 2	SP11/DP	11/4/75										

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BANK SELECTION									
S.A.	EA+1	W1	W2	W3	W4				
0	16K	OUT	OUT	OUT	IN				
1	16K	OUT	OUT	OUT	IN				
2	16K	OUT	OUT	OUT	IN				
3	16K	OUT	OUT	OUT	IN				
4	16K	OUT	OUT	OUT	IN				
5	16K	OUT	OUT	OUT	IN				
6	16K	OUT	OUT	OUT	IN				
7	16K	OUT	OUT	OUT	IN				
8	16K	OUT	OUT	OUT	IN				
9	16K	OUT	OUT	OUT	IN				
10	16K	OUT	OUT	OUT	IN				
11	16K	OUT	OUT	OUT	IN				
12	16K	OUT	OUT	OUT	IN				
13	16K	OUT	OUT	OUT	IN				
14	16K	OUT	OUT	OUT	IN				
15	16K	OUT	OUT	OUT	IN				
16	16K	OUT	OUT	OUT	IN				
17	16K	OUT	OUT	OUT	IN				
18	16K	OUT	OUT	OUT	IN				
19	16K	OUT	OUT	OUT	IN				
20	16K	OUT	OUT	OUT	IN				
21	16K	OUT	OUT	OUT	IN				
22	16K	OUT	OUT	OUT	IN				
23	16K	OUT	OUT	OUT	IN				
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25	16K	OUT	OUT	OUT	IN				
26	16K	OUT	OUT	OUT	IN				
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89	16K	OUT	OUT	OUT	IN				
90	16K	OUT	OUT	OUT	IN				



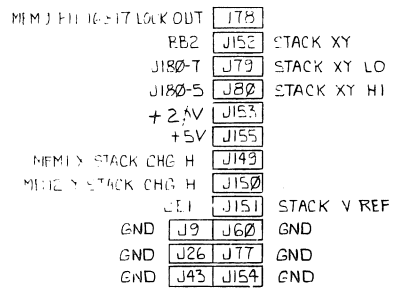
IC TYPE	GND	+5V	+20V SW
7400	1	16	
7401	8	16	
7402	8	16	
7403	9	16	
7404	8	16	
7405	12	5	
7406	12	5	
7407	12	5	
7408	12	5	
7409	12	5	
7410	12	5	
7411	12	5	
7412	12	5	
7413	12	5	
7414	12	5	
7415	12	5	
7416	12	5	
7417	12	5	
7418	12	5	
7419	12	5	
7420	12	5	
7421	12	5	
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7431	12	5	
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7435	12	5	
7436	12	5	
7437	12	5	
7438	12	5	
7439	12	5	
7440	12	5	
7441	12	5	
7442	12	5	
7443	12	5	
7444	12	5	
7445	12	5	
7446	12	5	
7447	12	5	
7448	12	5	
7449	12	5	
7450	12	5	
7451	12	5	
7452	12	5	
7453	12	5	
7454	12	5	
7455	12	5	
7456	12	5	
7457	12	5	
7458	12	5	
7459	12	5	
7460	12	5	
7461	12	5	
7462	12	5	
7463	12	5	
7464	12	5	
7465	12	5	
7466	12	5	
7467	12	5	
7468	12	5	
7469	12	5	
7470	12	5	
7471	12	5	
7472	12	5	
7473	12	5	
7474	12	5	
7475	12	5	
7476	12	5	
7477	12	5	
7478	12	5	
7479	12	5	
7480	12	5	
7481	12	5	
7482	12	5	
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7486	12	5	
7487	12	5	
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7493	12	5	
7494	12	5	
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7498	12	5	
7499	12	5	
7500	12	5	

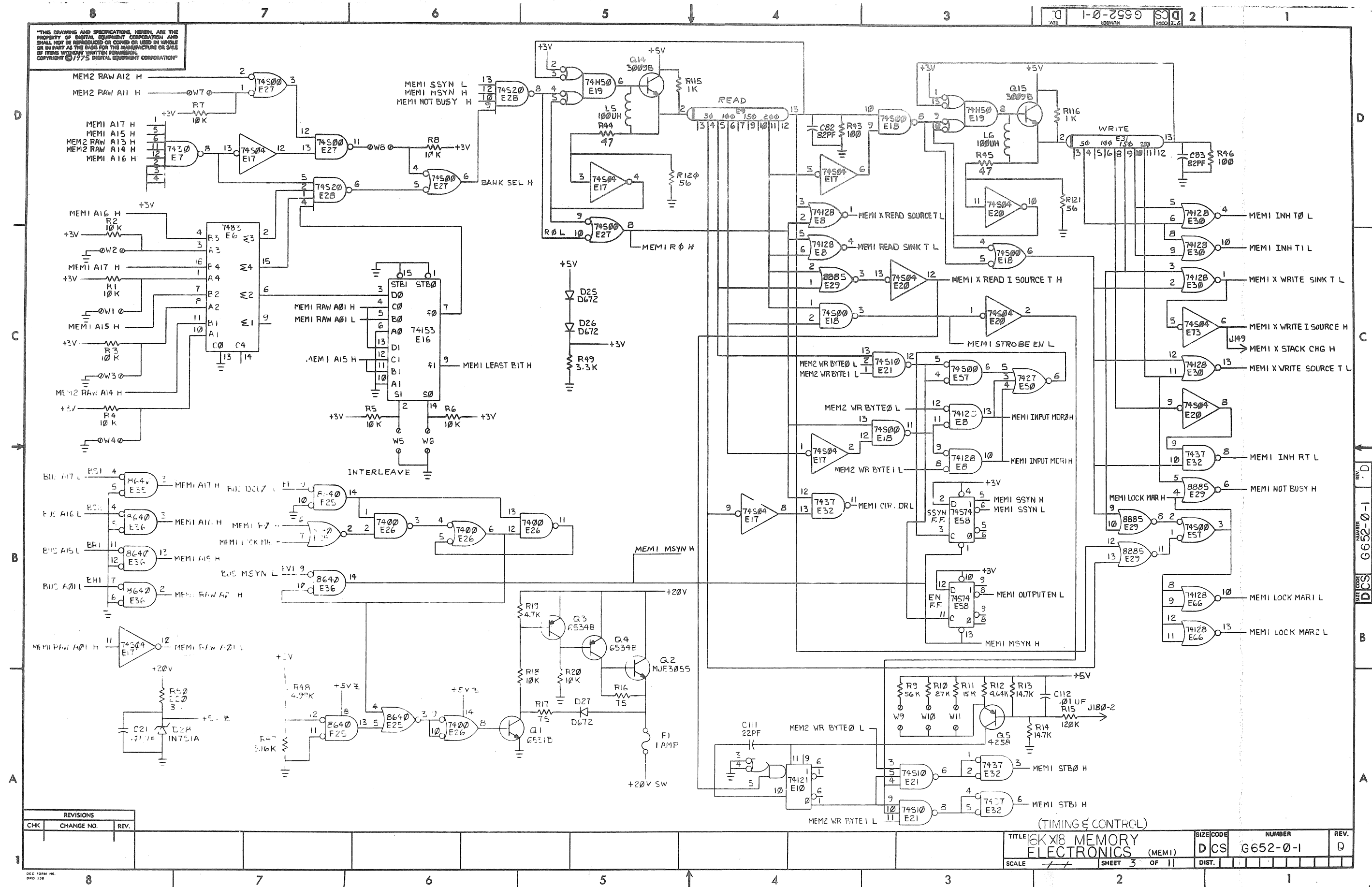
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPT WHERE STATED ABOVE

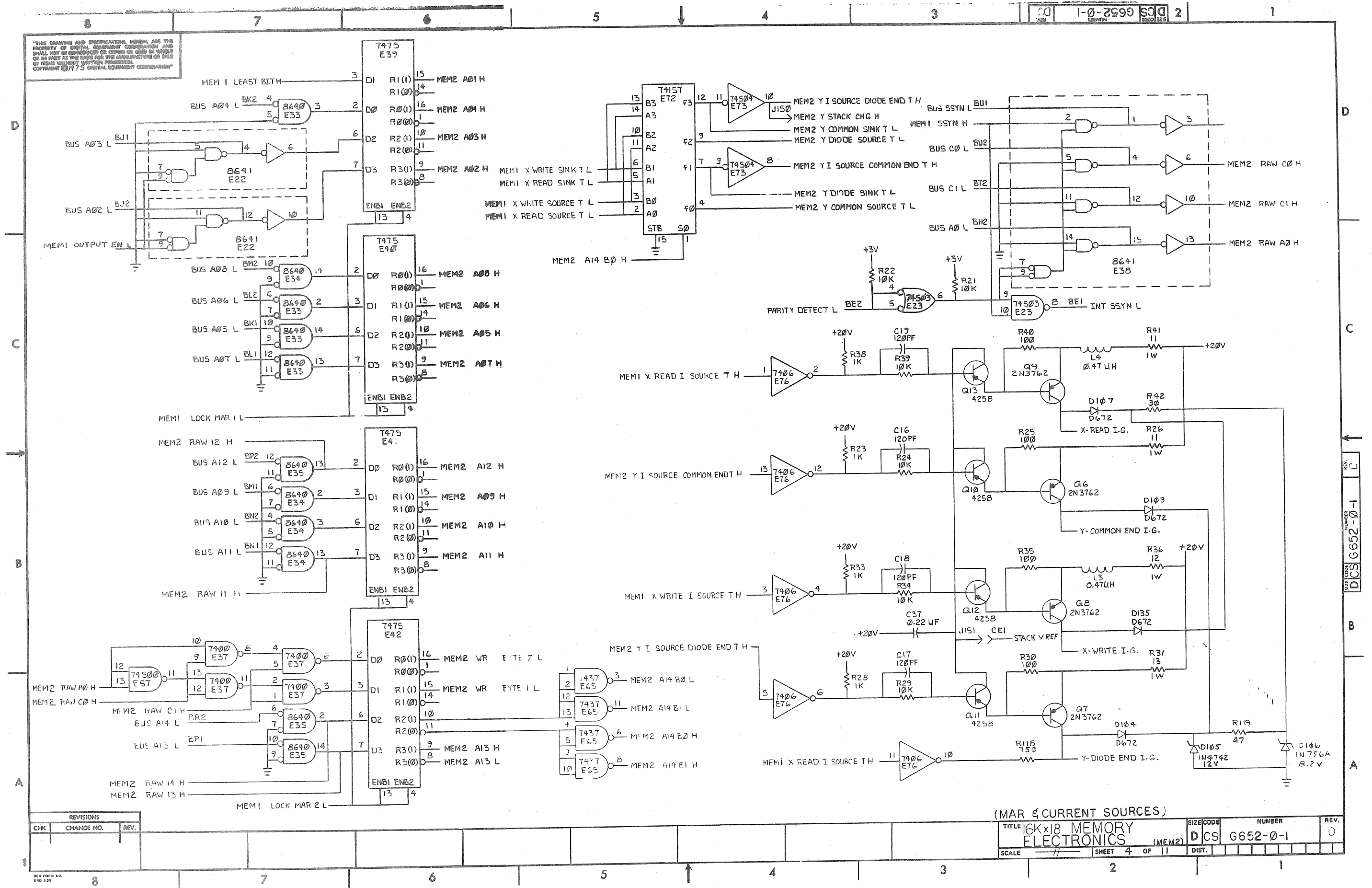
IC PIN LOCATIONS

NOTES:

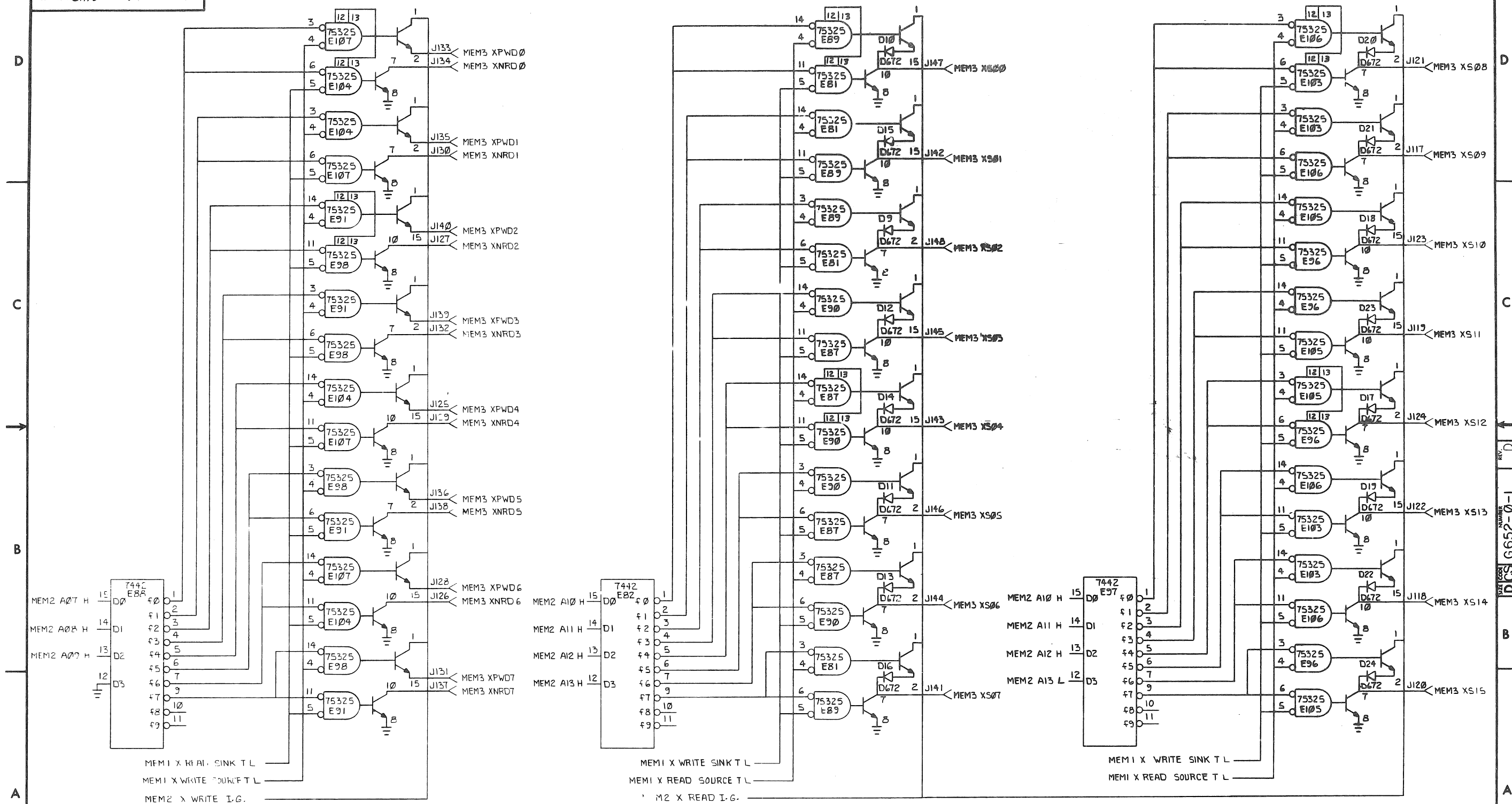
- INTERLEAVE CONTROL IS ACHIEVED AS FOLLOWS:
 - NON-INTERLEAVED
W5, W6 OUT
 - INTERLEAVED
W5 IN
W6 OUT } ONE MEMORY
W5 OUT
W6 IN } THE OTHER MEMORY
 - ADDRESS JUMPERS FOR THE COMBINED INTERLEAVED MEMORY ARE NOT THE SAME AS FOR THE NON-INTERLEAVED CASE (W1 THRU W4 MUST BE CUT THE SAME FOR BOTH INTERLEAVED MEMORIES). SEE BANK SELECTION CHART AT LEFT FOR BOTH INTERLEAVED AND NON-INTERLEAVED CASES.
 - UNDER NORMAL CONDITIONS (W8 OUT) THE MEMORY WILL NOT RESPOND TO ADDRESSES BETWEEN 124K AND 128K, BECAUSE THESE ADDRESSES ARE RESERVED FOR PERIPHERAL DEVICES ON THE UNIBUS. FOR SPECIAL APPLICATIONS WHERE THE RANGE OF PERIPHERAL ADDRESSES IS MORE RESTRICTED THAN THIS 4K RANGE THE MEMORY CAN BE CONFIGURED TO INCLUDE THE ADDITIONAL UNIBUS ADDRESSES AS FOLLOWS:
W8 IN } 124K TO 126K
W7 OUT }
W8 IN } 124K TO 127K
W7 IN }
- NOTE THAT FOR SYSTEMS WITHOUT MEMORY MANAGEMENT THIS MEANS THE USEFUL VIRTUAL ADDRESS SPACE FOR MEMORY CAN BE EXTENDED FROM 28K TO 30K OR 31K, BY SACRIFICING SOME PERIPHERAL SPACE.
- CAUTION**
SOME DIGITAL SOFTWARE MAY NOT BE COMPATIBLE WITH THE REDUCED PERIPHERAL ADDRESS SPACE. TO ASSURE COMPATIBILITY W8 MUST BE OUT.
3. SCHEMATIC FOR H222 MEMORY STACK IS SHOWN ON CS 5411554-0-1.







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REVISIONS		
CHK	CHANGE NO.	REV.

(X DRIVE)

TITLE	6K x 18 MEMORY ELECTRONICS
-------	----------------------------

SIZE	CODE
D	CS

NUMBER
62-0-1

REV.
D

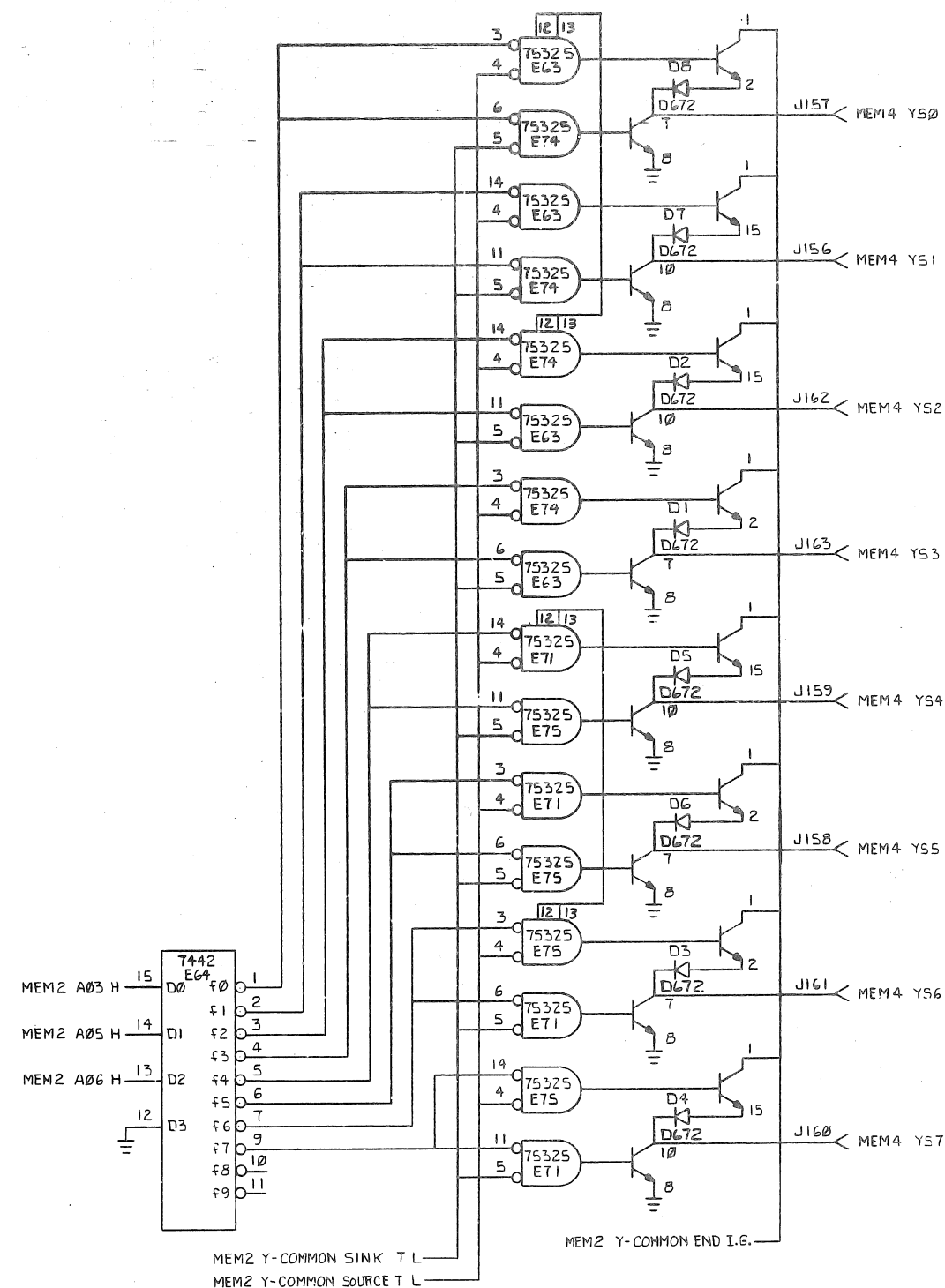
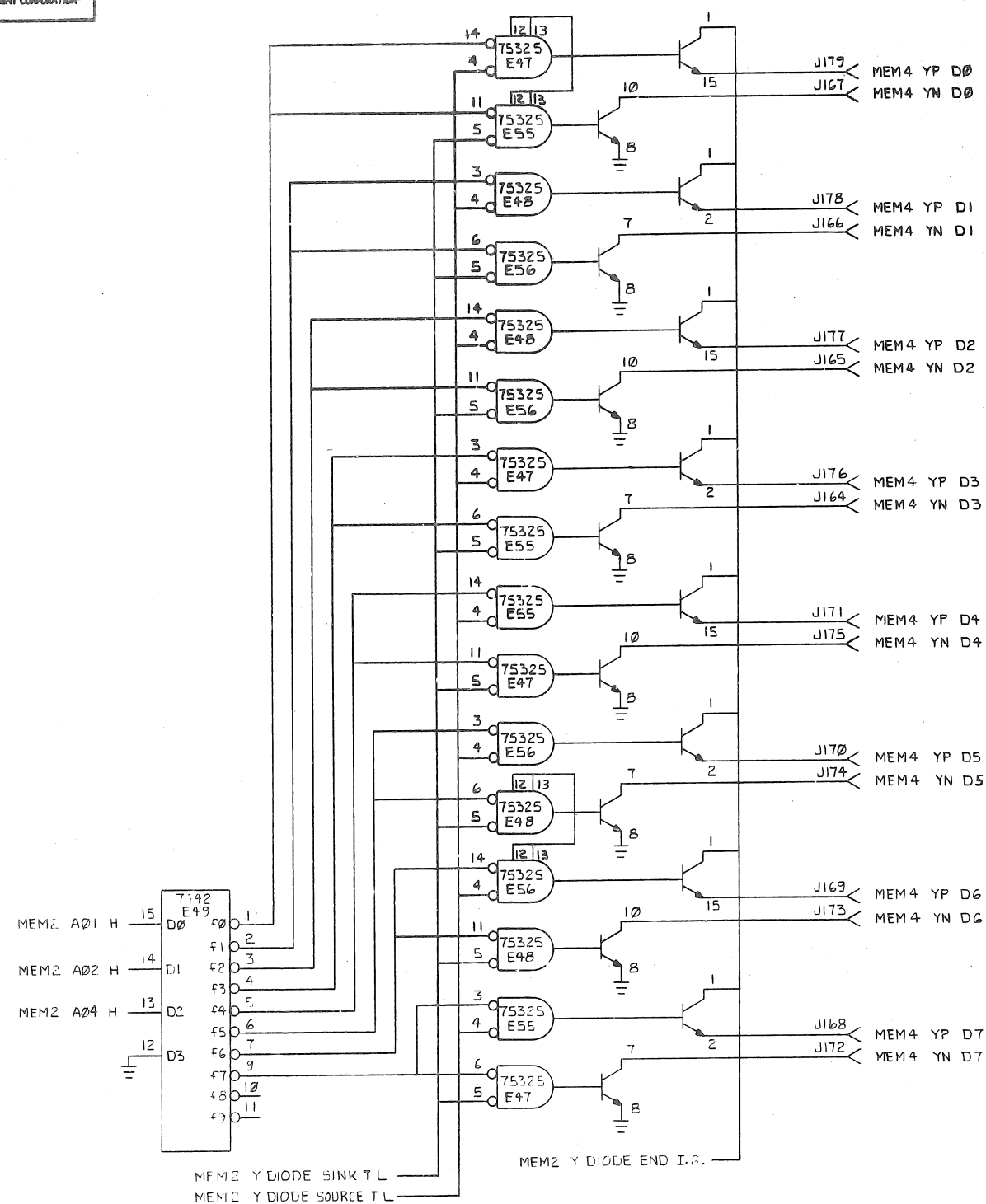
SCALE	<u>1/4" = 1'</u>	SHEET	5	OF	11
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	DIST.	
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REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO
DBD 130

(Y DRIVE)

TITLE	16K x 18 MEMORY ELECTRONICS
-------	--------------------------------

SIZE	CODE
D	CS

NUMBER
G652-0-1

RE	Q
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SCALE 1/4" SHEET 6 OF 11

SHEET 6 OF 11

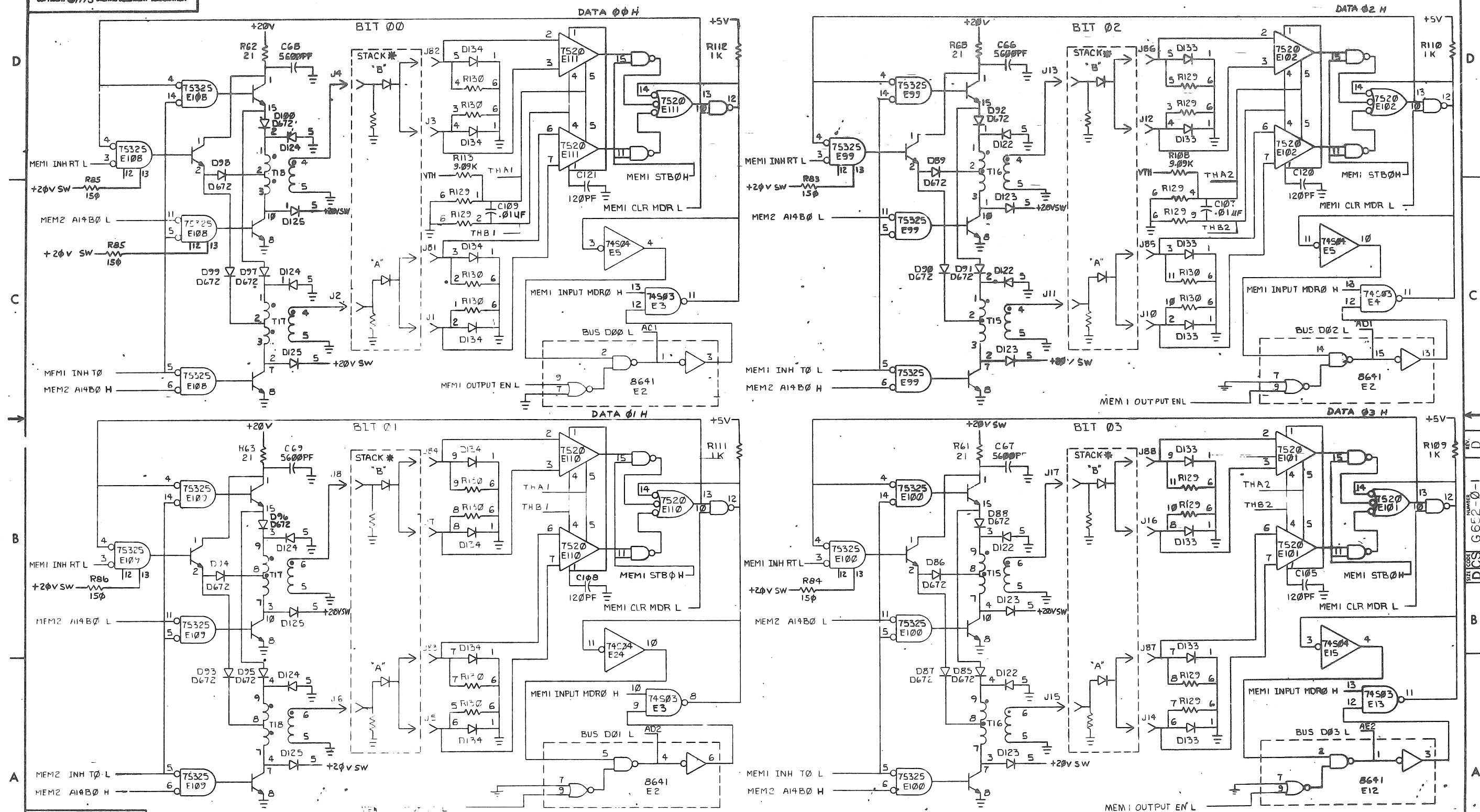
	DIST.	

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* MOUNTED ON H222 BD:

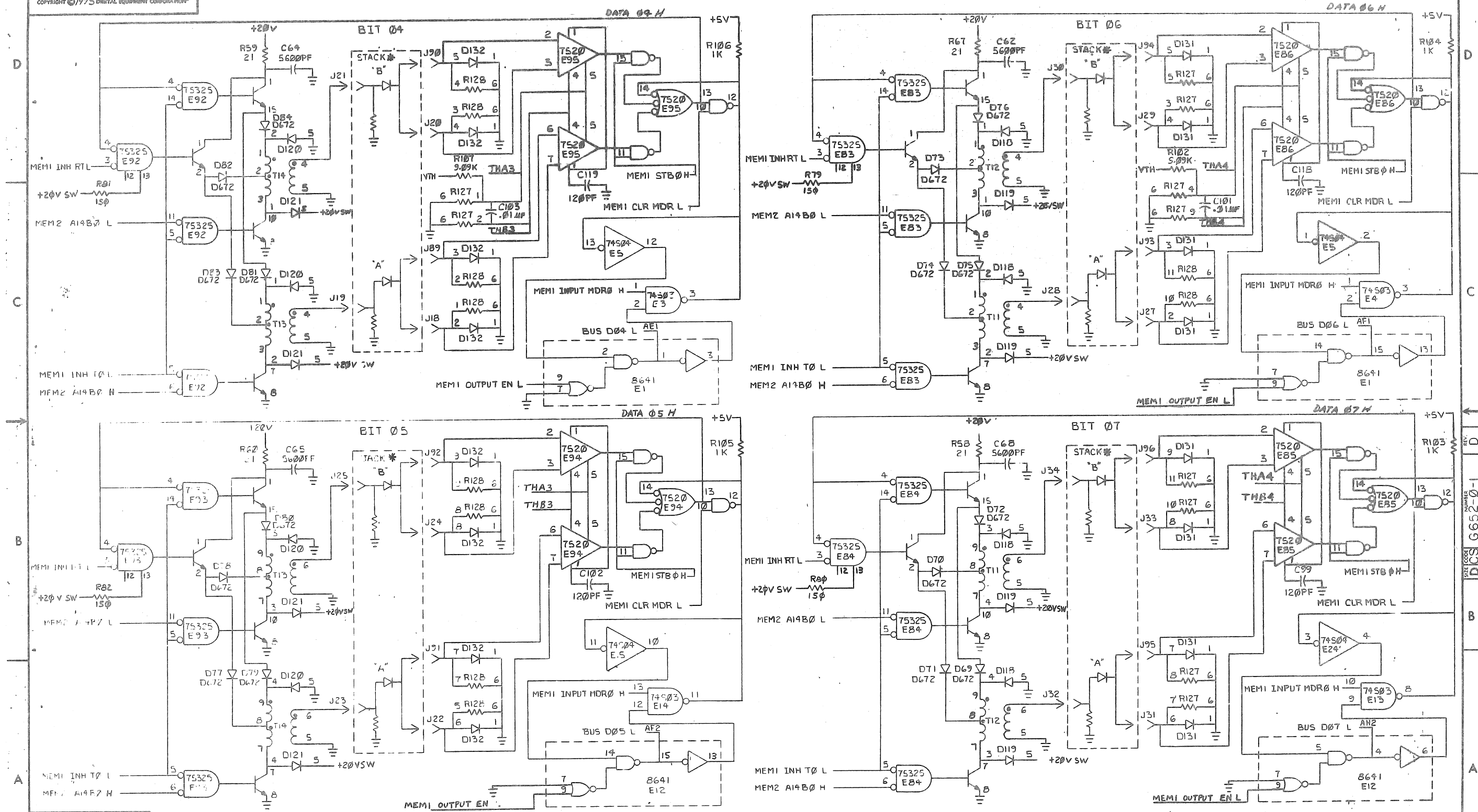


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		16Kx18 MEMORY ELECTRONICS	SIZE CODE	NUMBER	REV.
SCALE		1/4	SHEET	7 OF 11	D
DRAWN					
CHECKED					
APPROVED					

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* MOUNTED ON M222, BD



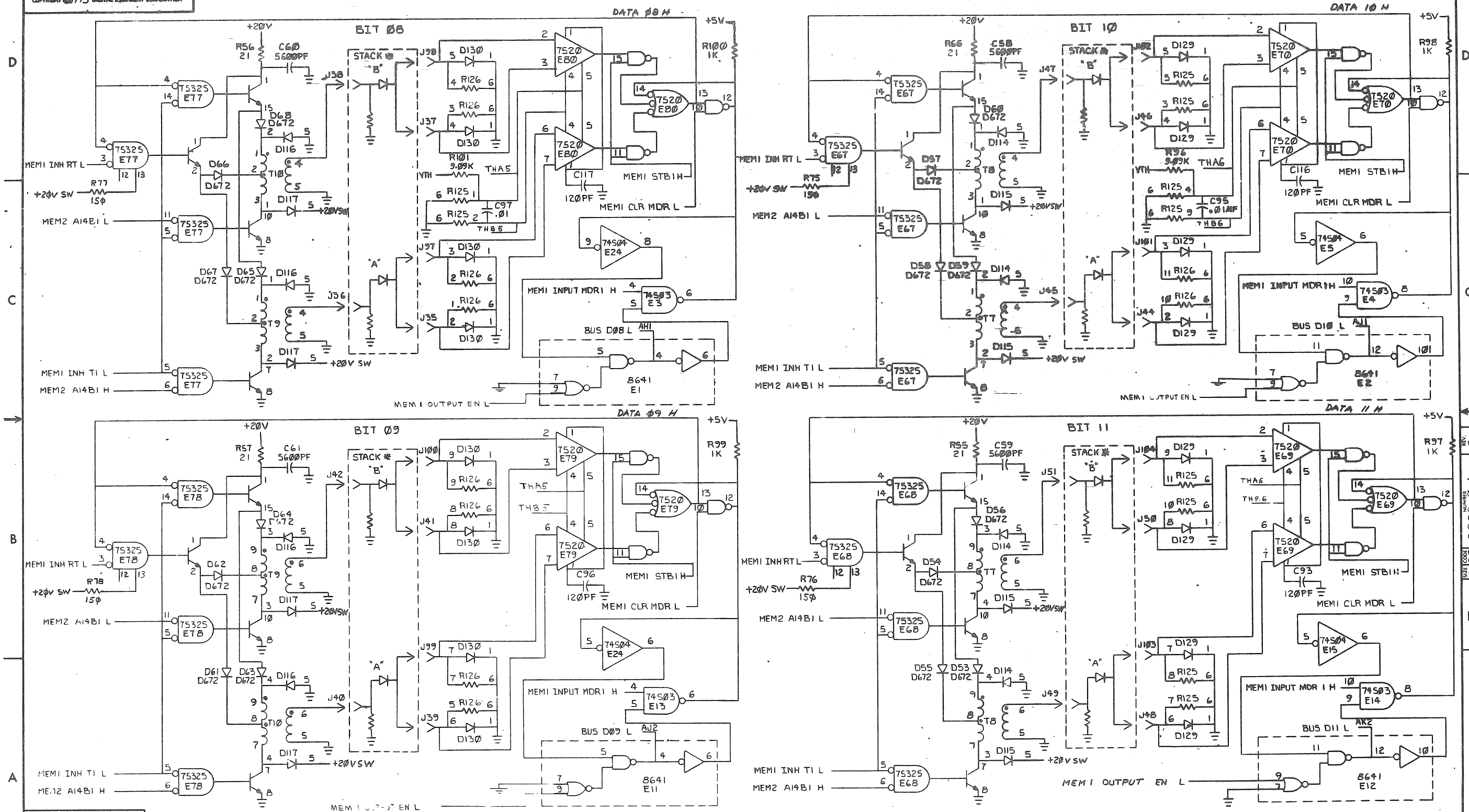
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE 6Kx18 MEMORY ELECTRONICS (MEM6)		SIZE CODE DCS	NUMBER G652-0-1	REV. D
SCALE	SHEET 8 OF 11	DIST.		

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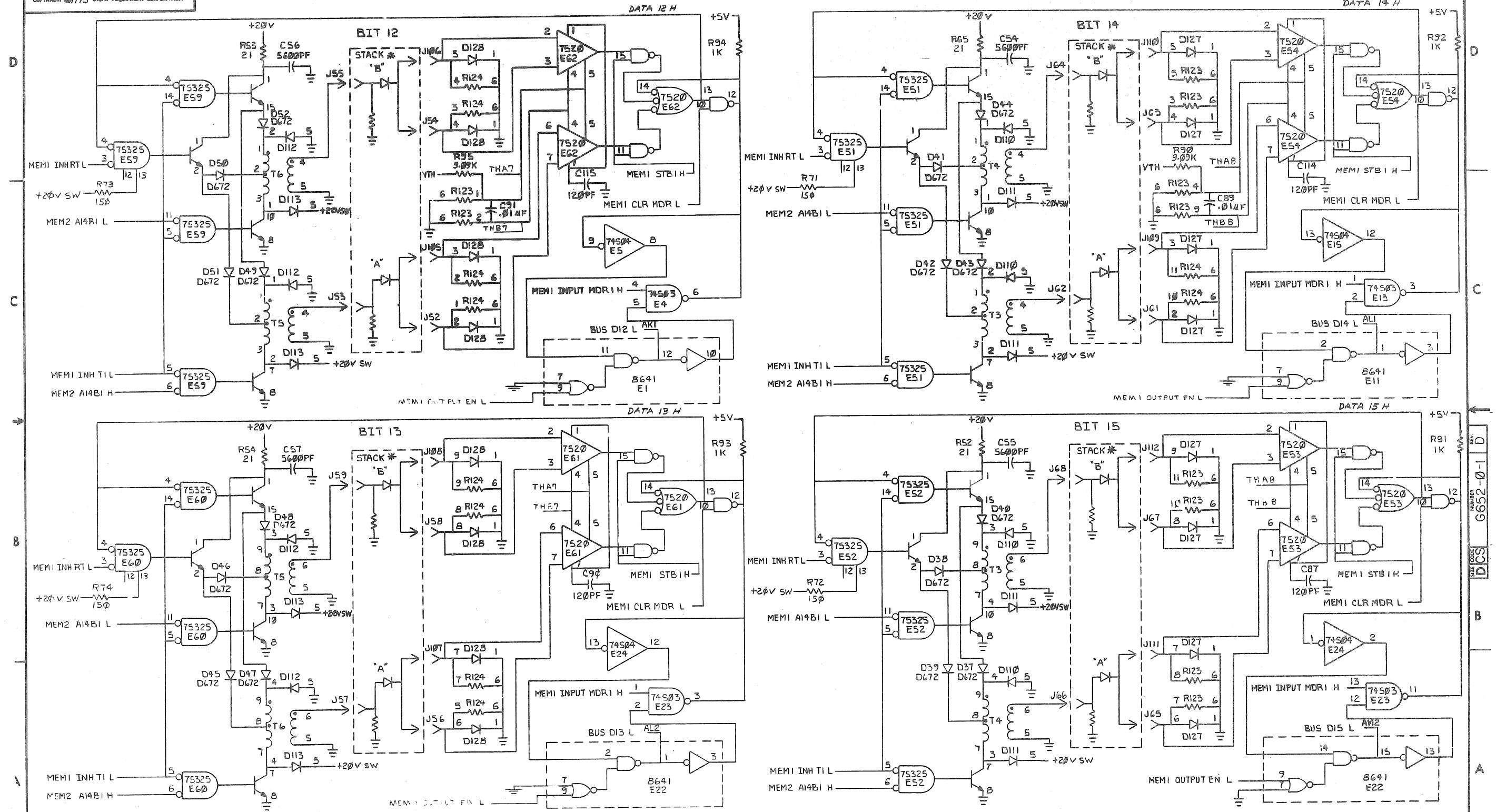
MOUNTED ON H122 BD :

1-0-2999 SCD 2



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MOUNTED ON H222 3D



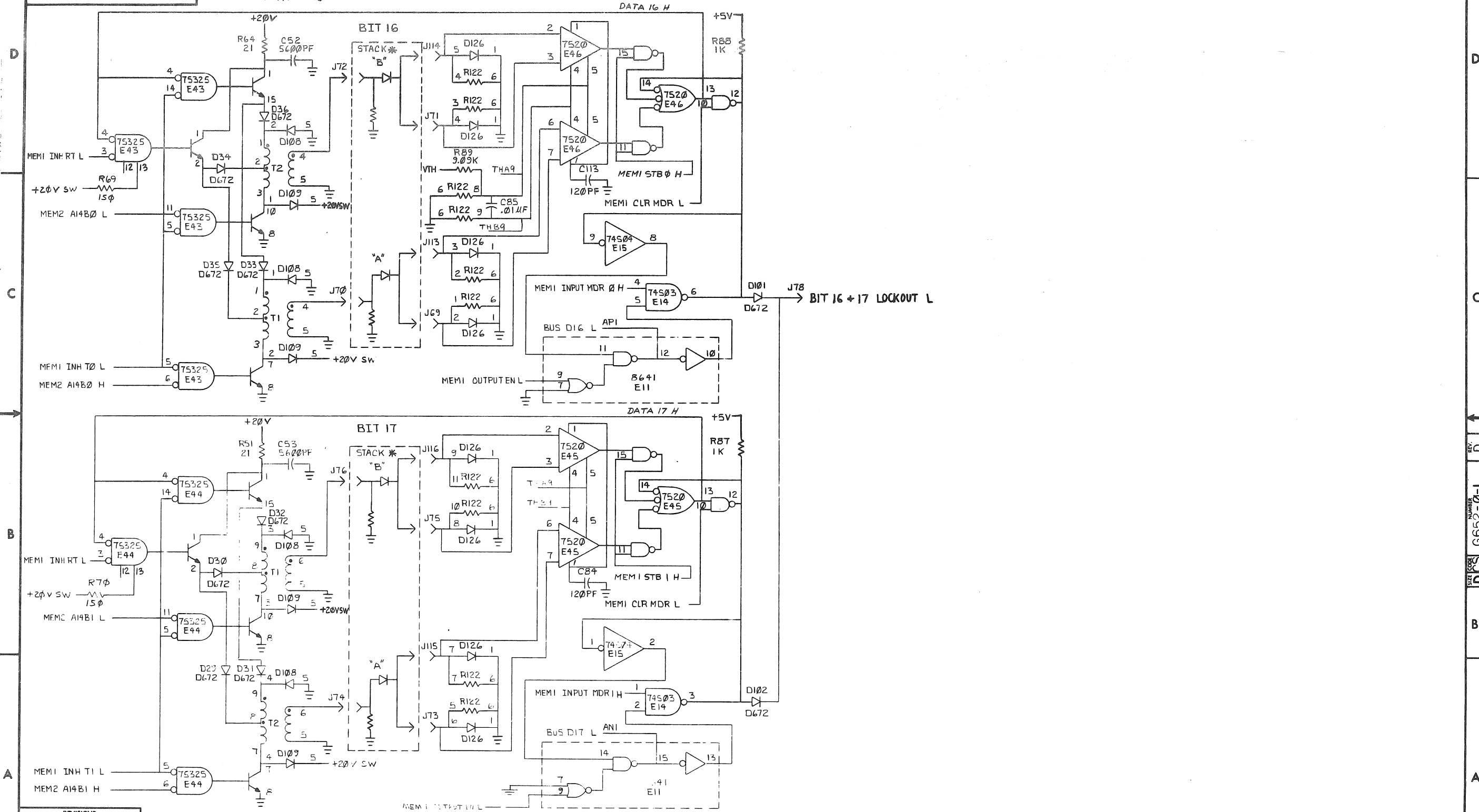
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE: 6Kx18 MEMORY ELECTRONICS (MEM1)		SIZE: CODE DCS	NUMBER: G652-0-1	REV. D
SCALE: 1/8"	SHEET: 10	OF: 11	DIST.:	

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* MOUNTED ON H222 BD.

1-0-2599 2

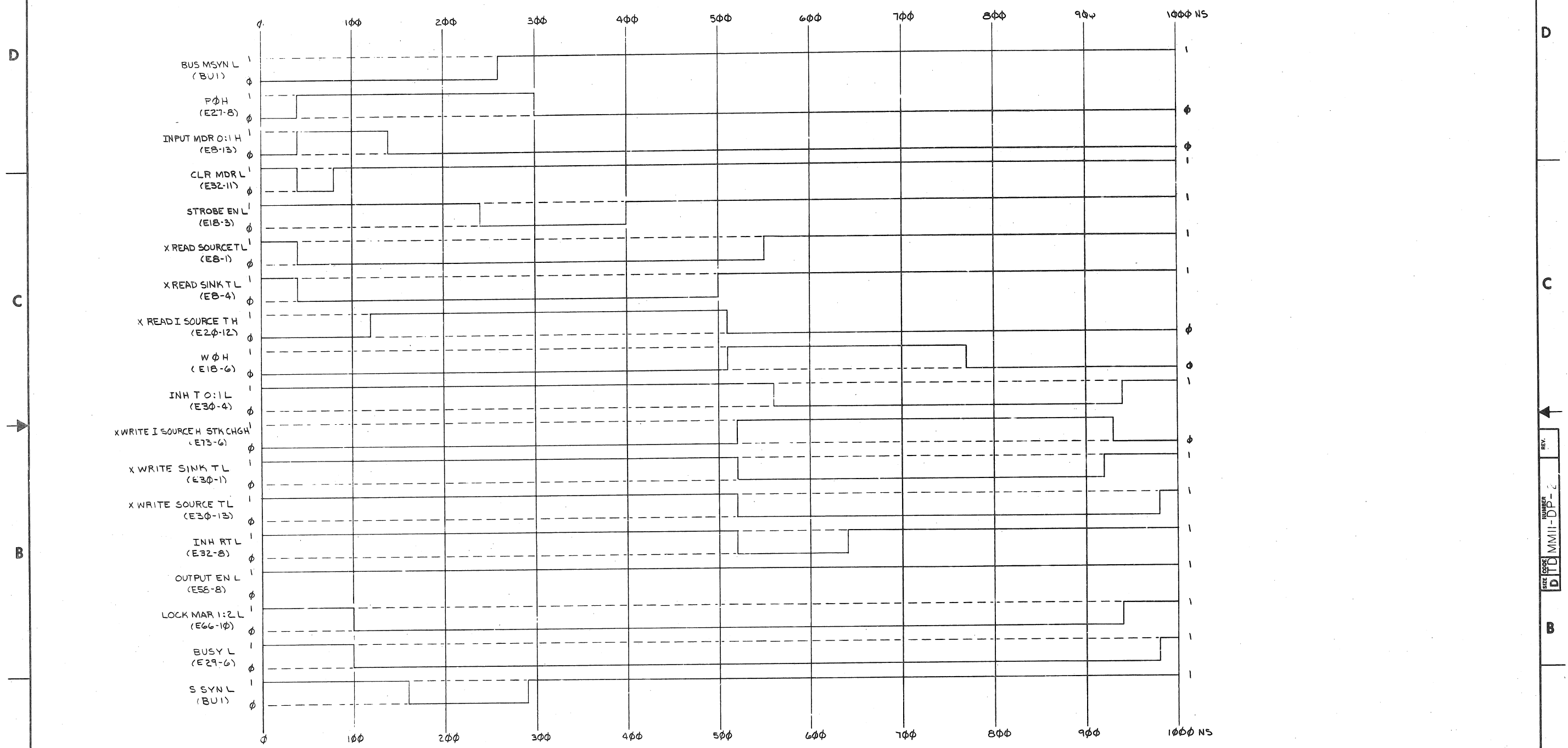


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
16Kx18 MEMORY		DCS	G652-0-1	D
ELECTRONICS (MEM9)		DIST.		
SCALE	SHEET	OF		

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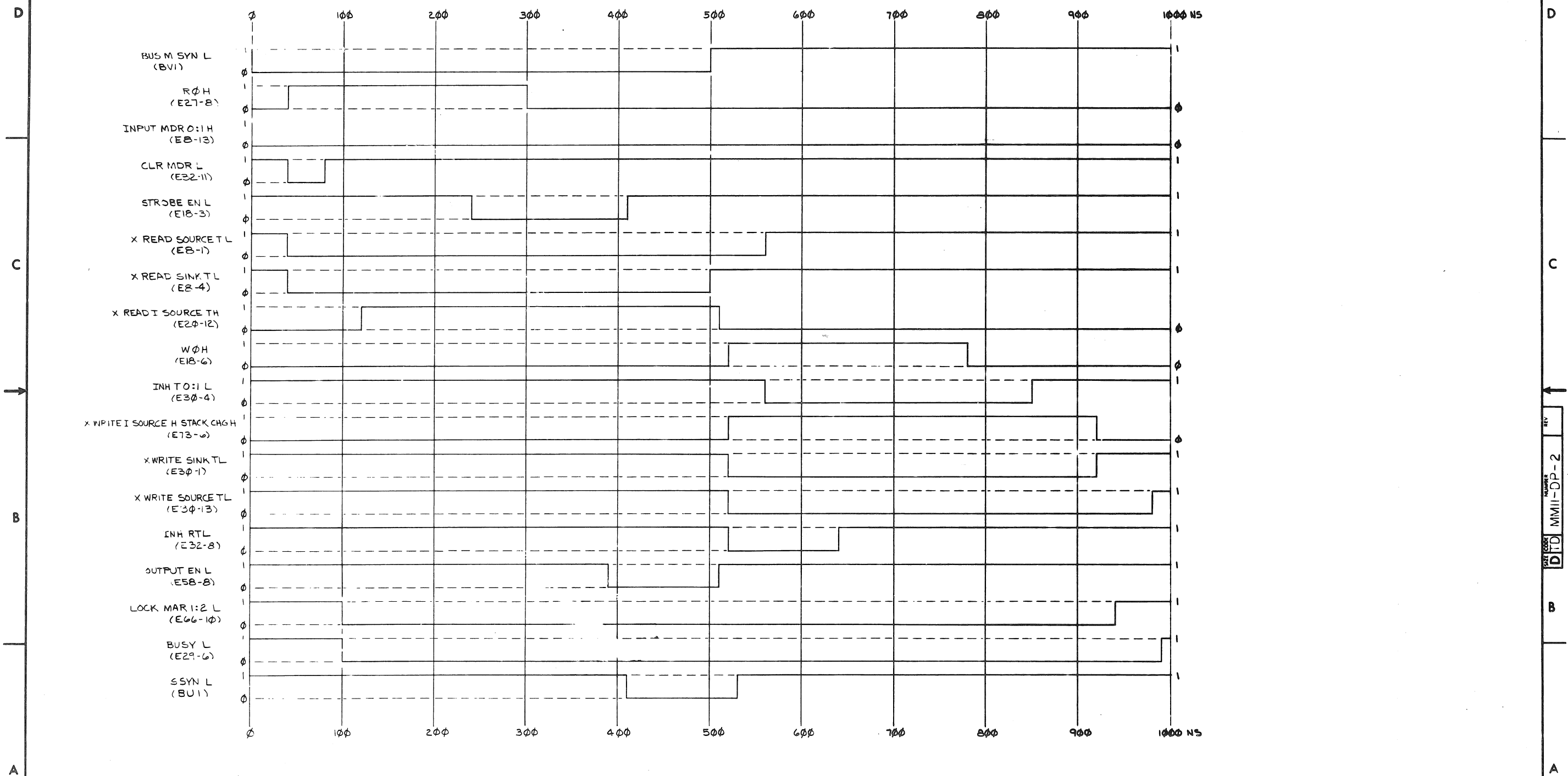
DAT 0



DRN. W. LUFKIN	10/20/75	FIRST USED ON	MMII-DP	digital
CHK'D. W. LUFKIN	10/20/75	TITLE	MMII-DP	
ENG. D. LUFKIN	10/20/75	NUMBER	MMII-DP-2	
PROJ. ENG. D. LUFKIN	10/20/75	REV.		
TIMING DIAGRAM				
NEXT HIGHER ASSY.				
B-DD-MMII-DP				
SCALE +				
SHEET 1 OF 2				

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DAT I

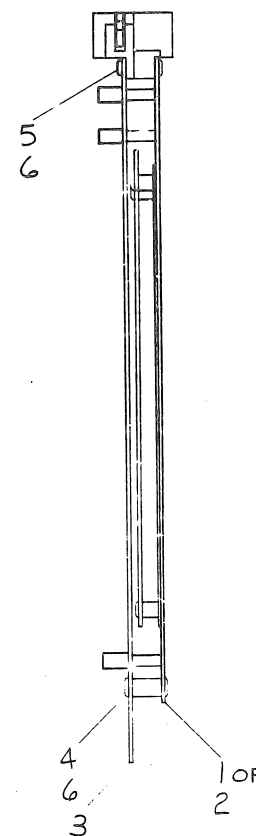
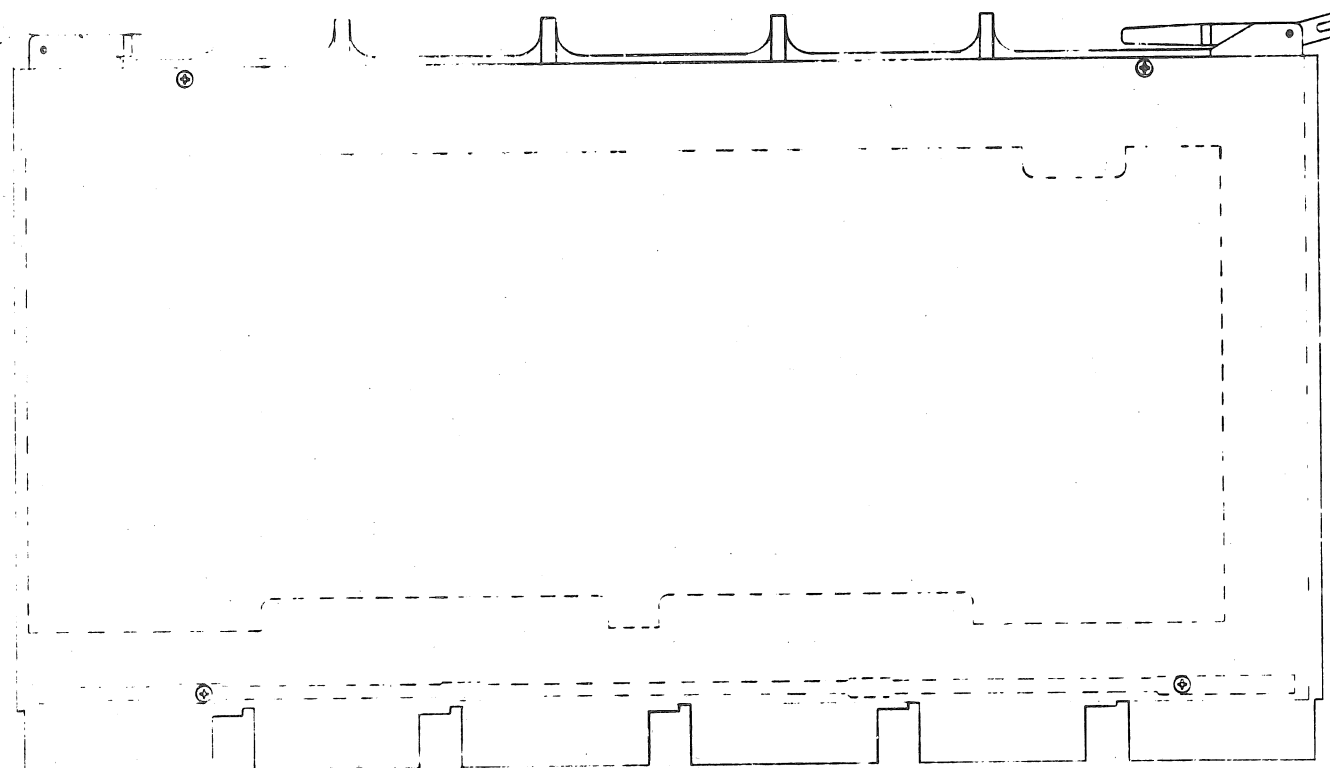


REV
MMII-DP-2
D

REVISIONS		
CHK	CHANGE NO.	REV

NOTE :

1. MMII-DP MEMORY MODULE ASSEMBLY (16K X18 BIT)
MAY BE SUBSTITUTED FOR MMII-D MODULE (16K X16 BIT)



4	4	WASHER, LOCK #2 INT TOOTH	9006631-A0	6
2	2	SCR, PHL PAN HD, SS #2-56 x .31 LG	9006002-C	5
2	2	SCR, PHL PAN HD, SS #2-56 x .19	9006000-C1	4
1	1	16K X18 CORE MEMORY	D-CS-3652-0-1	3
1	1	H222 STACK BOARD (16 BIT)	D-UA-H222-B-0	2
1	1	H222 STACK BOARD (18 BIT)	D-UA-H222-B-0	1

		DESCRIPTION		DWG. PART NO.		ITEM NO.	
		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES					
		NOMINAL DIMENSION RANGE INCHES					
		CLASS OF ACCURACY		OVER 1	OVER 1	OVER 1	OVER 1
		(CHECK ONE)		0.1	0.2	0.4	0.6
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
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				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5	0.8	1.0
				0.3	0.5		

THIRD ANGLE PROJECTION		DRN. H. LUFKIN 7/24/75		FIRST USED ON		MM11-DP		digit	
		CHK'D <i>[Signature]</i> 8/21/75		TITLE		MM11-DP			
REMOVE BURRS AND BREAK SHARP CORNERS		ENG. <i>[Signature]</i> 8/21/75		PROJECT		MEMORY MODULE			
DO NOT SCALE DWG		PROJ. ENG. <i>[Signature]</i> 8/21/75		PROD.		ASSEMBLY			
MATERIAL SEE PARTS LIST		NEXT HIGHER ASSY.		SIZE		CODE		NUMBER	
FINISH		B-DD-MM11-DP		D		JA		MM11-DP 3	
		SCALE 1/1		SHEET		1 OF 1		DIST.	

THIS IS PRINT SET

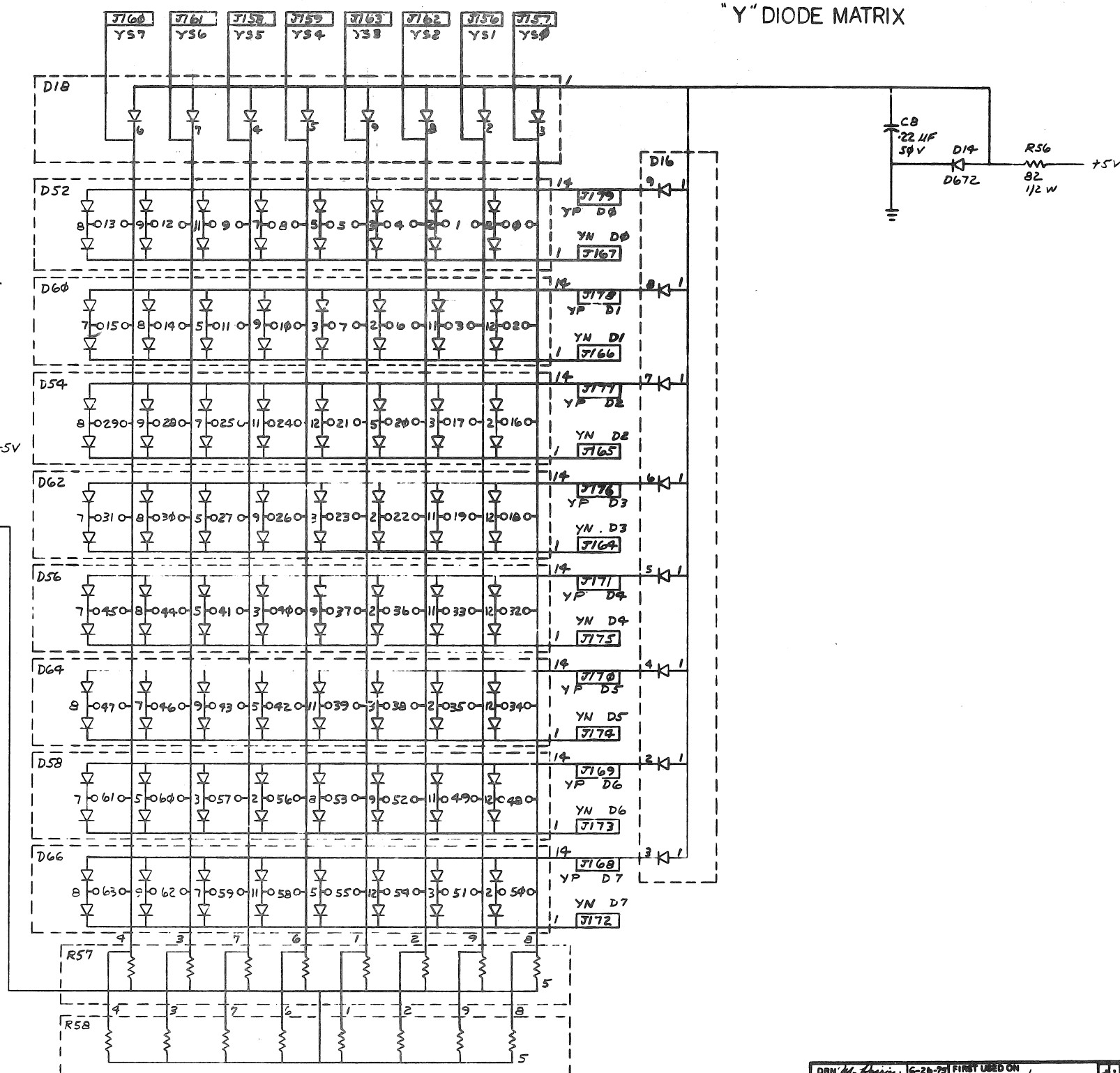
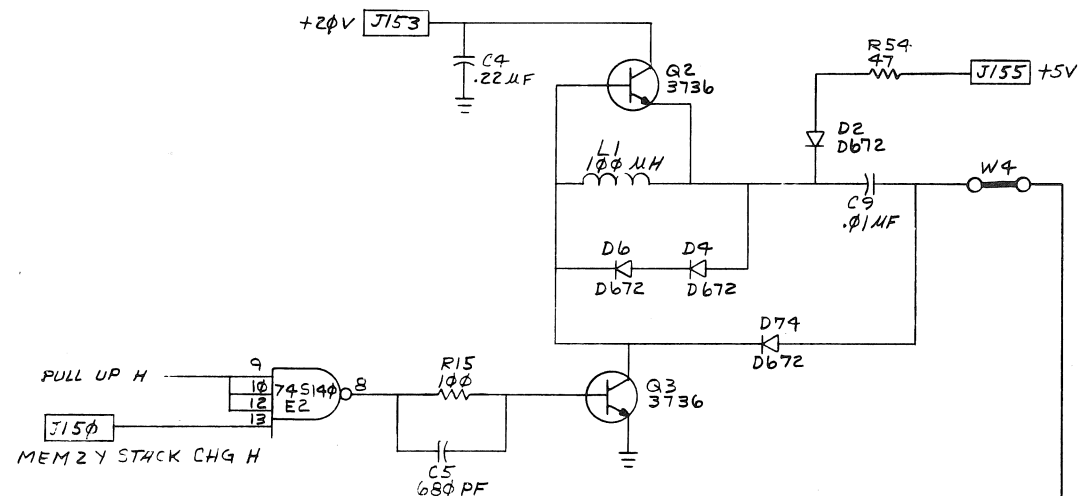
SEQUENCE

[illegible]

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NOTE:
PREFIX FOR J156-J179 IS MEM4.

"Y" DIODE MATRIX



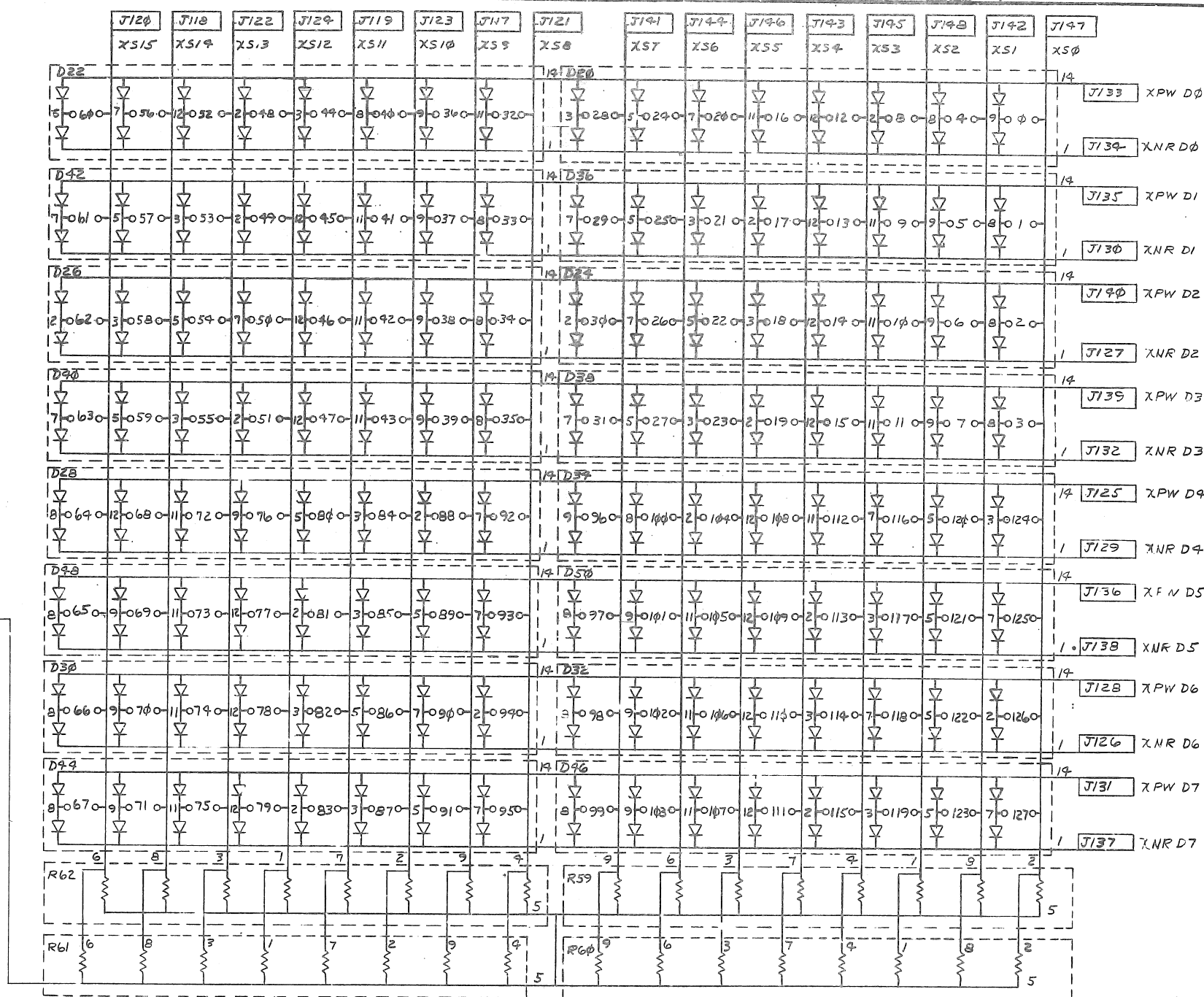
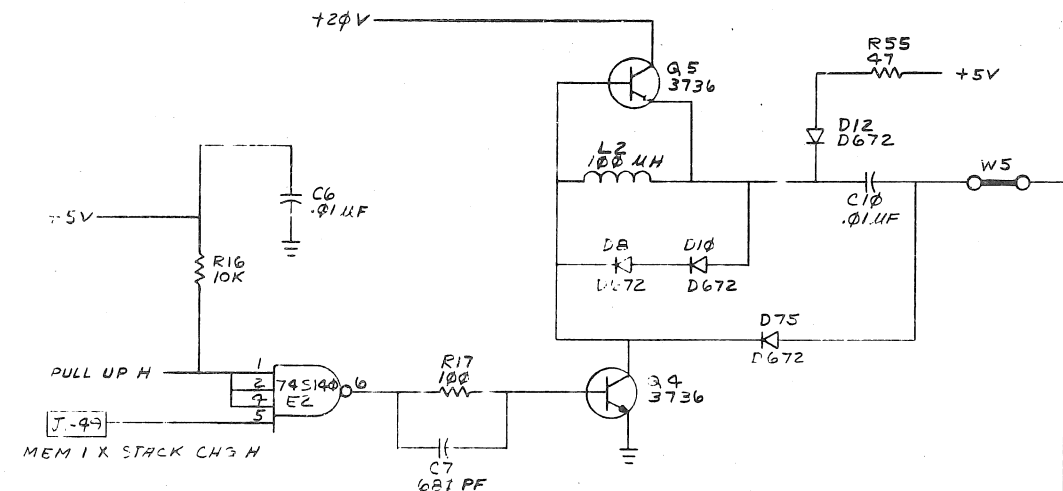
DRN. M. F. 6-26-75	FIRST USED ON	MMI/D/DP
CHK'D. J. L. 7/1/76	TITLE	16K X 16, 18
ENG. D. L. 7/1/76	PROD. R. W. 7/1/76	MEMORY STACK
NEXT HIGHER ASSY.		
D-UA-H222-0	SIZE CODE	NUMBER
SCALE	D CS	5411554-0-1
SHEET 1 OF 3	DIST.	REV. C

REVISIONS		
CHK	CHANGE NO.	REV.
ER	5411554-0000.	C
H.D. Smelser 6-9-76		
D. SMELSER		
D. Smelser 6/16/76		

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"X" DIODE MATRIX

NOTE:
PREFIX FOR J117-J148 IS MEM9.

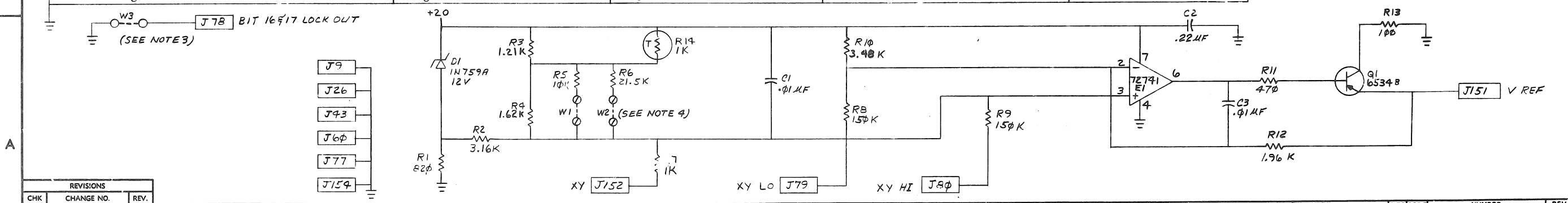
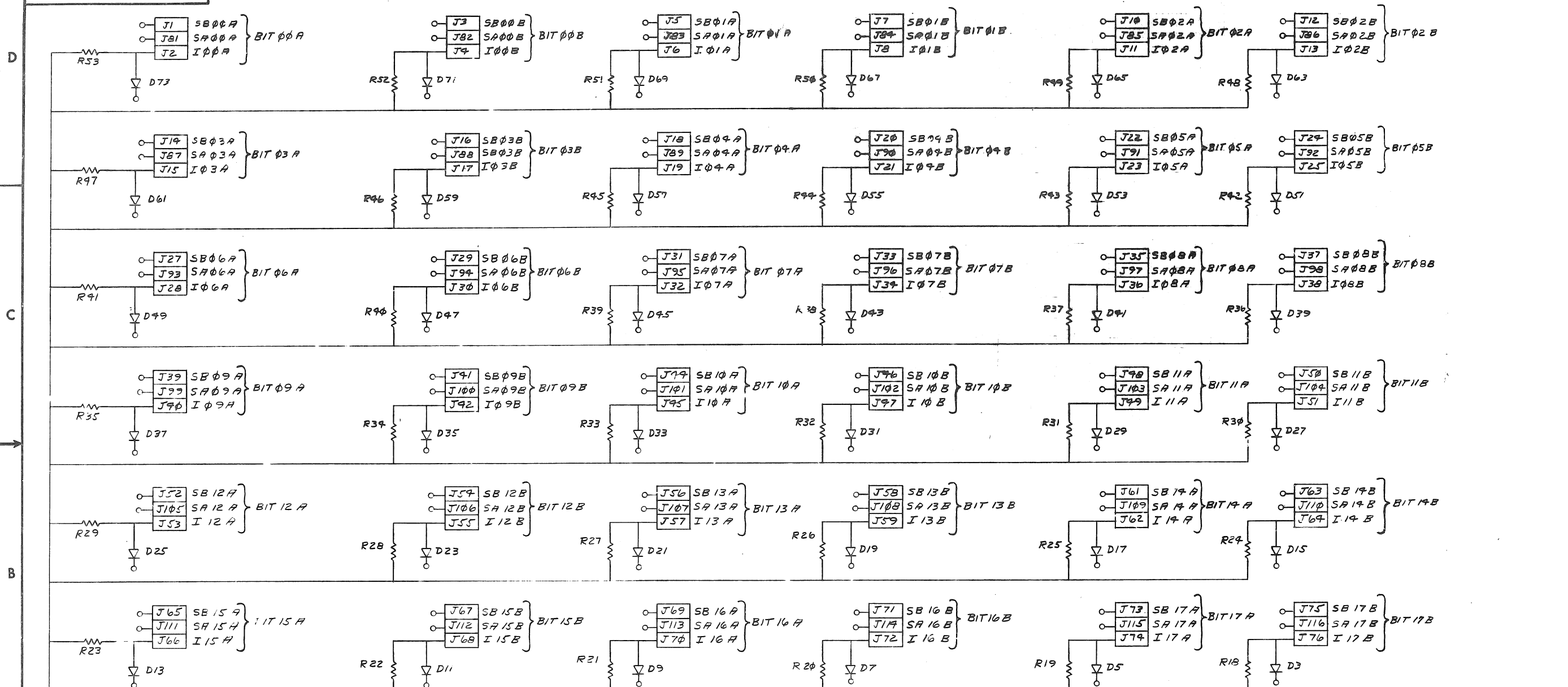


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	16K X 16.18 MEMORY STACK	SIZE CODE	NUMBER	REV.
SCALE	1/1	SHEET	2 OF 3	DIST.

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- NOTES:
1. R18 THRU R53 ARE 1K, 1/4W $\pm 5\%$.
 2. UNLESS OTHERWISE SPECIFIED:
ALL DIODES ARE D672.
 3. W3 MUST BE INSTALLED FOR A 16 BIT MEMORY -- H222B, OR H222A USED WITHOUT A PARITY CONTROLLER (M7B50).
 4. W1 & W2 ARE USED TO ADJUST VREF.

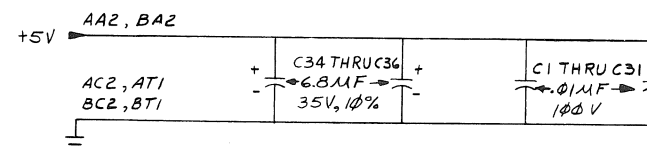
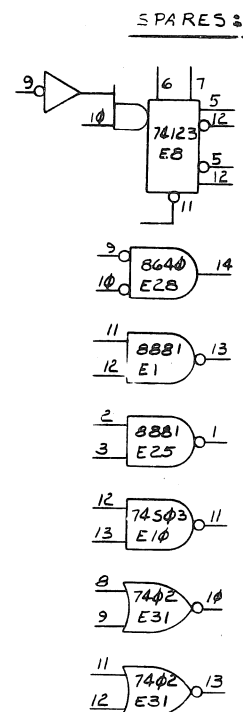
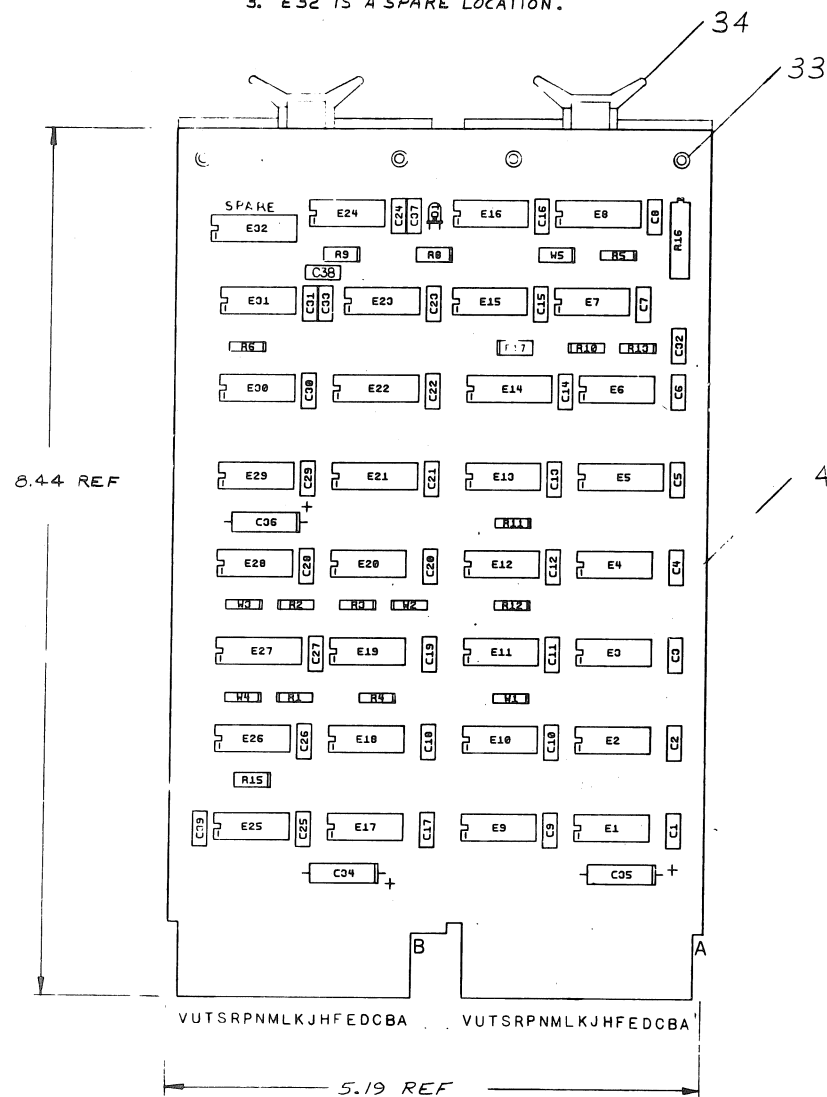


REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO
DRD 100-C

THIRD ANGLE PROJECTION	DRN. W. LUFKIN 7/17/75	FIRST USED ON MM11-DP		DATE		A	
	CHK'D <i>W. Lufkin</i> 8/21/75	TITLE		H222 CORE MEMORY STACK ASSEMBLY			
REMOVE BURRS AND BREAK SHARP CORNERS	ENG. <i>W. Lufkin</i> 12/1/75	PROD. <i>W. Lufkin</i> 3/22/76					
	PRO. <i>W. Lufkin</i> 12/1/75						
DO NOT SCALE DWG	NEXT HIGHER ASSY.						
MATERIAL <i>SEE PARTS LIST</i>	D-VA-MM11-DP-Ø	SIZE D	CODE VA	NUMBER H222-Ø-Ø	REV. A		
FINISH + + +	SCALE 1 / 1	SHEET 1 OF 1		DIST.			

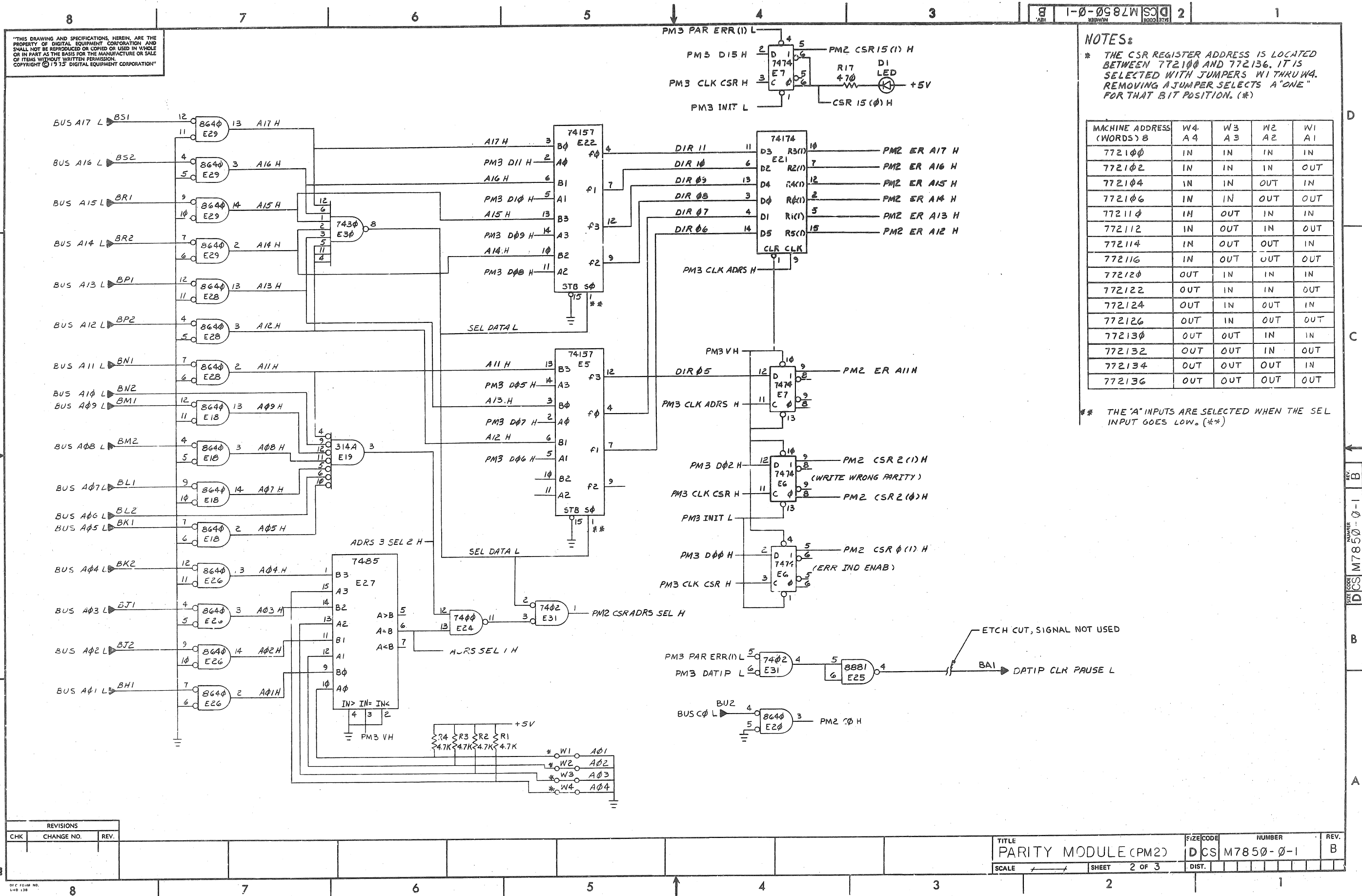
1. ALL RESISTORS 1/4W, 5% UNLESS OTHERWISE NOTED.
ALL CAPACITORS ARE 100V, 20% UNLESS OTHERWISE NOTED.
2. JUMPER CONTROL OPERATIONS AS FOLLOWS:
W1 THRU W4 SELECT CSR ADDRESS, W5 CAPACITOR
FOR S SYN DLY, NOT USED.
3. E32 IS A SPARE LOCATION.



REF	CIRCUIT SCHEMATIC	D-CS-M7850-0-1	REF
REF	X-Y COORDINATE HOLE LOCATION	K-CO-M7850-0-4	1
REF	ASSY/DRILLING HOLE LAYOUT	D-AH-M7850-0-5	2
REF	MODULE ECO HISTORY	B-MH-M7850-0-6	3
1	ETCHED CIRCUIT BOARD	5010651-00	4
1 C39	CAP 470 PF, 100V, 5% (DM)	1000024-00	5
2 C33, C37	CAP 330 PF, 100V, 5% (DM)	1000023-00	6
3 C1 THRU C32	CAP .01 MF, 100V, 20% DISC	1001610-01	7
3 C34, C35, C36	CAP 6.8 MF, 35V, 10% STANT	1005306-00	8
1 D1	DIODE, L.E.D.	1110324-00	9
5 R1 THRU R5	RES 4.7K 1/4W 5%	1300447-00	10
3 R8, R9, R15	RES 100 1/4W 5%	1300229-00	11
5 R6, R10, R11, R12, R17	RES 470 1/4W 5%	1300316-00	12
1 R13	RES 1K 1/4W 5%	1300365-00	13
1 R16	RES 10K 3/4W 242 (76 PR. 100)	1309143-10	14
1 E24	I.C. 7400	1905575-00	15
1 E30	I.C. 7430	1905578-00	16
1 E31	I.C. 7402	1909004-00	17
1 E19	I.C. 314A	1909704-00	18
1 E23	I.C. 7408	1910155-00	19
4 E1, E9, E17, E25	I.C. 8881	1909705-00	20
1 E16	I.C. 74H04	1909931-00	21
1 E27	I.C. 7485	1910224-00	22
1 E8	I.C. 74123	1910436-00	23
1 E10	I.C. 74503	1910533-00	24
1 E15	I.C. 74574	1910544-00	25
1 E21	I.C. 74174	1910652-00	26
2 E5, E22	I.C. 74157	1910655-00	27
9 E2, E3, E11, E12, E18 E20, E26, E28, E29	I.C. 8640	1911469-00	28
2 E4, E13	I.C. 745280	1911573-00	29
1 E14	I.C. 8266	1909934-00	30
2 E6, E7	I.C. 7474	1905547-00	31
4 W1 THRU W4	INSULATED JUMPER	9009185-00	32
4	EYELET	9006732-00	33
2	HANDLE, FLIP-CHIP (MAGENTA)	9008337-6	34

QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV.	B			
		DRN. W. LUFKIN	DATE 2-5-75	 TITLE PARITY MODULE
		CHK'D <i>W. Lufkin</i>	DATE 2/17/75	
		ENG. <i>W. Lufkin</i>	DATE 2-18-75	
		PROD. ENG. <i>W. Lufkin</i>	DATE 2-18-75	
		PROD. <i>W. Lufkin</i>	DATE 2-18-75	
		NEXT HIGHER ASSY		
		DEC NO.	EIA NO.	
		SCALE		
CONVERSION CHART		SHEET	OF 3	
		SIZE CODE	NUMBER	REV.
		DCS	M7850-0-1	B
		DNT.		

20 40	1	5
30 60	3	16
40 57	5	16
41 4	3	16
42 3	5	16
43 5	8	16
5 47	1	5
IC TYPE	GND	+5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTIONS ARE STATED ABOVE		
IC PIN LOCATIONS		



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NOTES:
* THE CSR REGISTER ADDRESS IS LOCATED BETWEEN 772100 AND 772136. IT IS SELECTED WITH JUMPERS W1 THRU W4. REMOVING A JUMPER SELECTS A "ONE" FOR THAT BIT POSITION. (*)

MACHINE ADDRESS (WORDS) B	W4 A4	W3 A3	W2 A2	W1 A1
772100	IN	IN	IN	IN
772102	IN	IN	IN	OUT
772104	IN	IN	OUT	IN
772106	IN	IN	OUT	OUT
772110	IN	OUT	IN	IN
772112	IN	OUT	IN	OUT
772114	IN	OUT	OUT	IN
772116	IN	OUT	OUT	OUT
772120	OUT	IN	IN	IN
772122	OUT	IN	IN	OUT
772124	OUT	IN	OUT	IN
772126	OUT	IN	OUT	OUT
772130	OUT	OUT	IN	IN
772132	OUT	OUT	IN	OUT
772134	OUT	OUT	OUT	IN
772136	OUT	OUT	OUT	OUT

** THE 'A' INPUTS ARE SELECTED WHEN THE SEL INPUT GOES LOW. (**)

CHK	CHANGE NO.	REV.	TITLE	SIZE CODE	NUMBER	REV.
			PARITY MODULE (PM2)	DCS	M7850-0-1	B
			SCALE	SHEET	2 OF 3	

