

decsystem10

Advanced Systems Group

TECHNICAL DATA SHEET

12/72

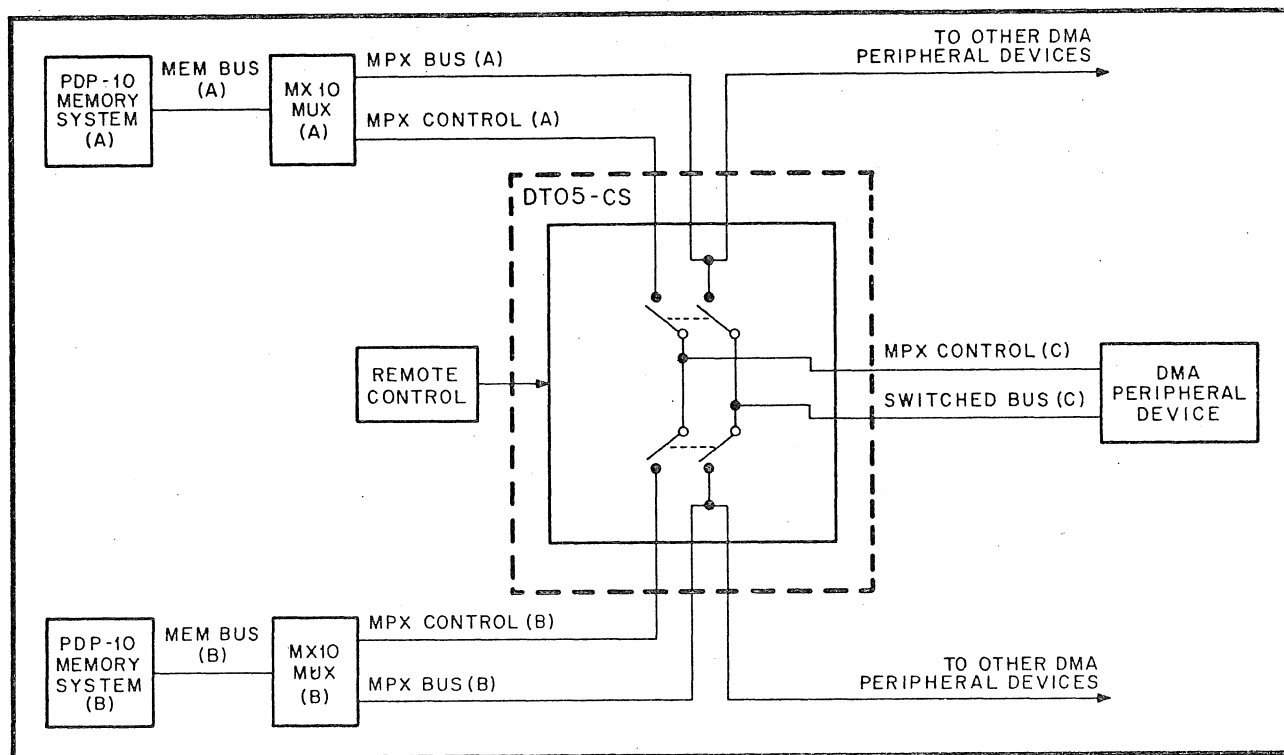
DT05-CS MEMORY BUS SWITCH

FEATURES

- Electronic Memory Bus Switch
- Used for Switching KA10 Memory Bus

The DT05-CS Memory Bus Switch is a solid-state switching device that allows either of two PDP-10 memory buses or MX10 Multiplexer buses to communicate with a direct-memory-access (DMA) device connected to a third (shared) memory or multiplexer bus. Switching of the DT05-CS is controlled by either local or remote contact closures.

The remote mode is the normal operation in which the switching of the DT05-CS is controlled from a remote switch panel or from a programmable controller such as the DT03-CC. For local mode, two switches are provided to select one of two memory or multiplexer buses. The DT05-CS is composed basically of two sets of transistor gates; one set is associated with each multiplexer.



10-1003

Typical System

SPECIFICATIONS

Mechanical:

Cabinet	Mounts in a DF 10 Data Channel Cabinet
Logic Panels	Two, Type H911
Dimensions	10-1/2 in. h, 19 in. w, 6-3/4 in. d
Weight	50 lb

Electrical:

Power Supply and Control	Controlled and Provided by DF 10
Power Dissipation	215W (180W at low duty factor)
Logic Potentials	+10V, -15V
Module Series	B- and W-Series

Operational:

Switched Positions	Memory Bus A On/Off Memory Bus B On/Off
Normal Combinations	(1) Memory Bus A On, Memory Bus B Off (2) Memory Bus A Off, Memory Bus B Off (3) Memory Bus A Off, Memory Bus B On
Transfer Type	Bidirectional bus transfers allowed when switch combination (1) or (3) above is selected
Control	Local or Remote Contact Closures
No. of Signals Switched	Seventy-Four

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