POWER INPUT

BAUD RATE CLOCK GENERATOR

PROCESSOR CLOCK FOR Z80 AND
PERIPHERAL CHIPS

CLOCK SOURCES

VIDEO CLOCKS

Figure E-4 (Sheet 1 of 6)
Logic Board Revision C
Clock Generation and
Power Input

Schematic Diagrams E-21/E-22
KEYBOARD INTERFACE

KEYBOARD CLOCK GENERATOR

VCC

VOD

GND

VOD

VAA

D3

HOST AND PRINTER PORTS

BAUD RATE GENERATOR AND VERTICAL INTERRUPT HANDLER

CURRENT LOOP AND RS422 SOCKET

HOST PORT

PRINT PORT

Figure E-4 (Sheet 6 of 6)
Logic Board Revision C
Host, Printer and Keyboard Serial Channels
Schematic Diagrams
E-31/E-32
Figure E-1 (Sheet 1 of 1)
42 Watt Switching Power Supply

Schematic Diagrams  E-3/E-4

NOTE: All resistors are 5% tolerance unless otherwise specified.

C2i 1000μF later versions (E2J F)

2N2102 will do
NOTE: Monitor board is the same as ESPIRIT 6310
EHT transformer part no 1-492-024-01 is
a separate item.

Figure E-5 (Sheet 1 of 1)
V220/V240
Monitor Schematic

Schematic Diagrams E-33/E-34